

Delay Insensitive Circuits

an exercise in formal methods

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Delay insensitive circuits are understandable by:

- (a) intuitive unconvincing hand-wavy descriptions
- (b) formal theories checkable by programs or proofs

Let's try to find a way from (a) to (b).

The intuitive approach

All you need to know about delay insensitive circuits:

- they have no clocks
- handshake signals and causal relationships make them go
- theory taught in school doesn't work on them
- they are feared, reviled, ignored, and misunderstood

Executive summary

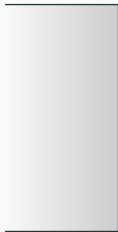
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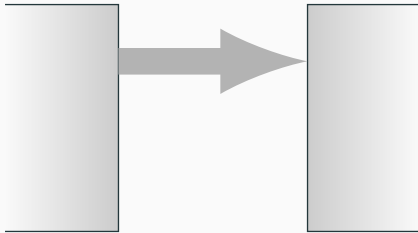
Let's go to work !



Synchronization by handshakes

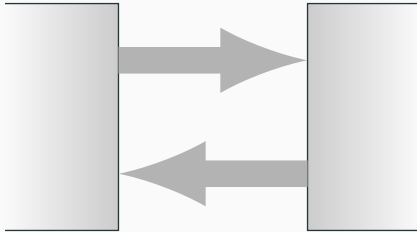


Synchronization by handshakes



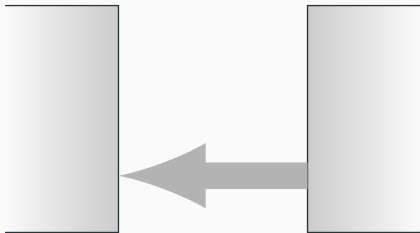
- request

Synchronization by handshakes



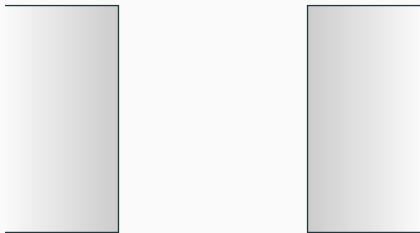
- request
- acknowledge

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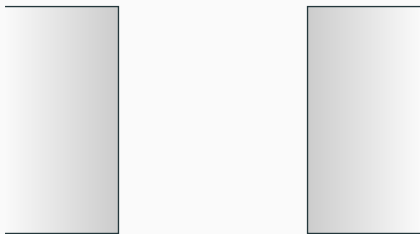
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- release

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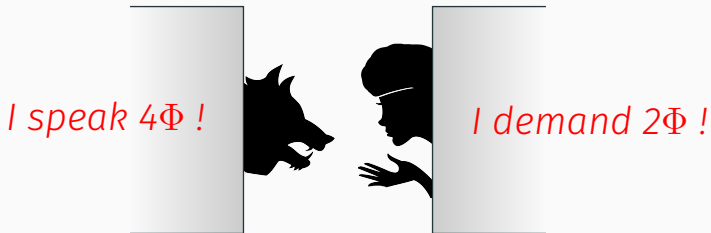
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Synchronization by handshakes



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- } unnecessary ?

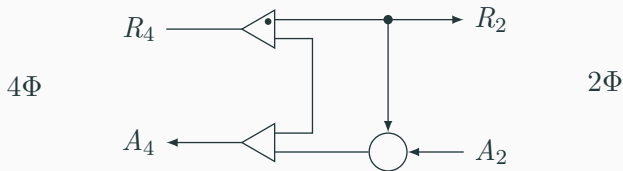
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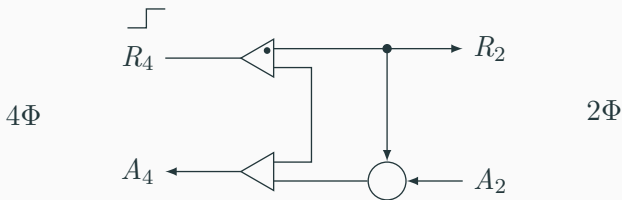
4 Φ to 2 Φ handshake converter

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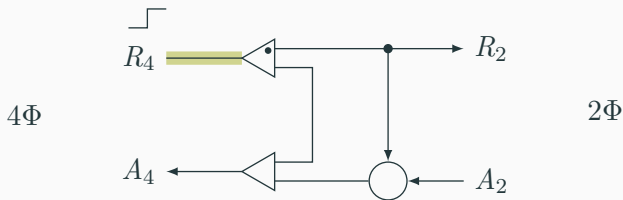
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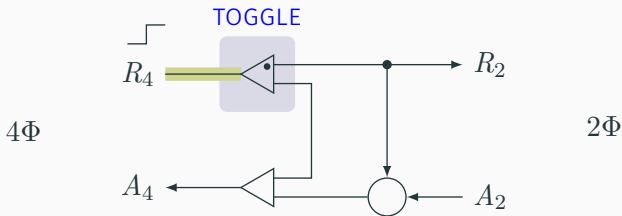
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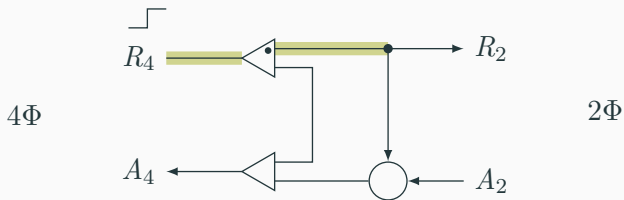
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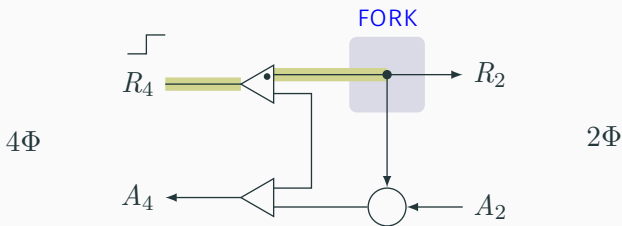
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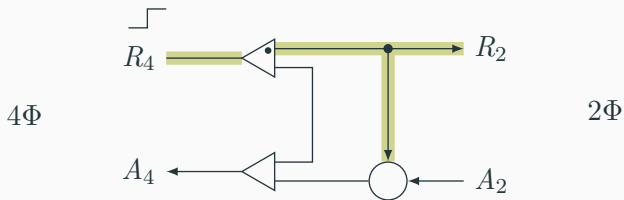
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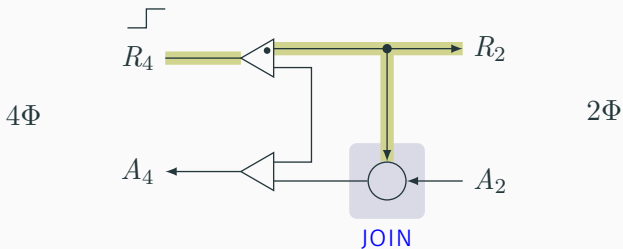
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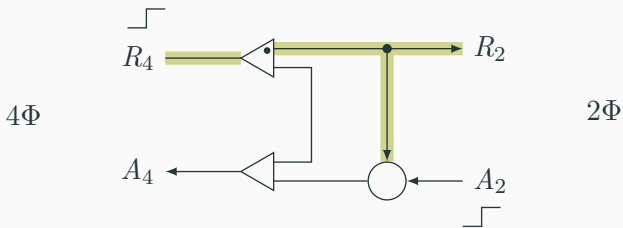
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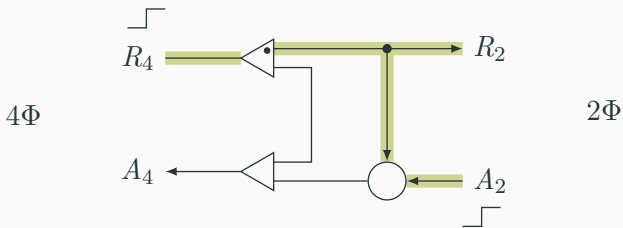
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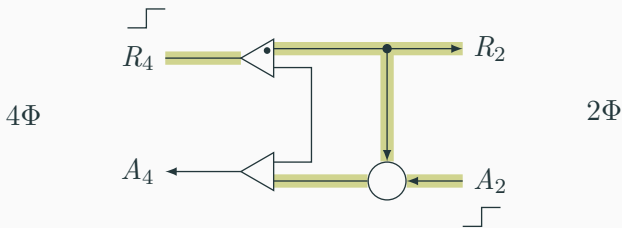
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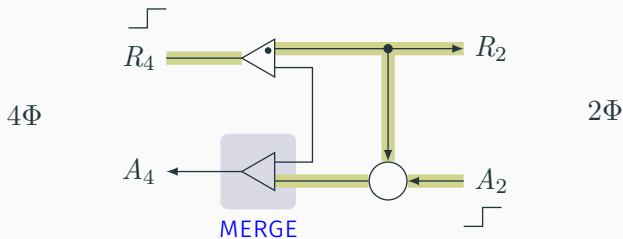
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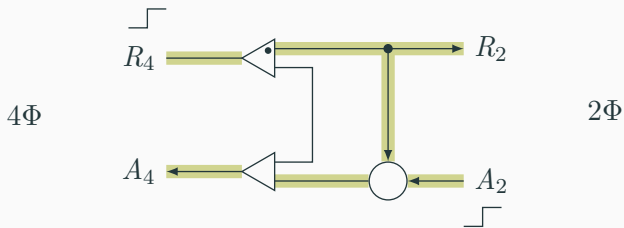
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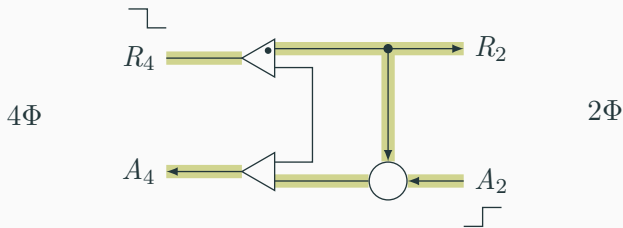
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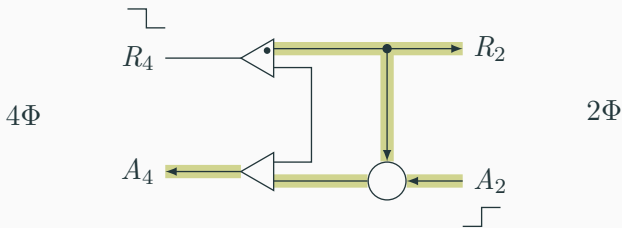
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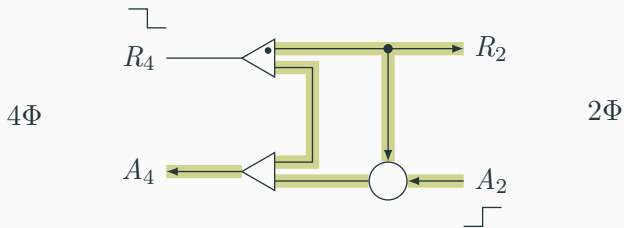
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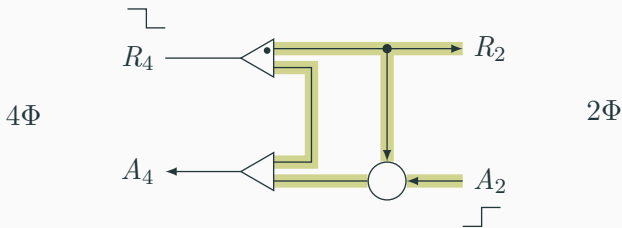
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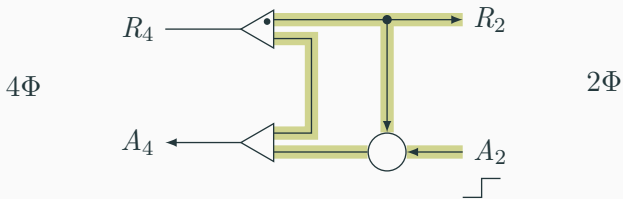
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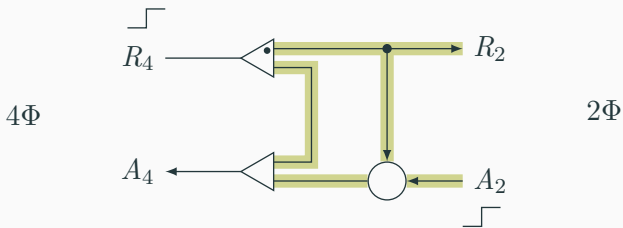
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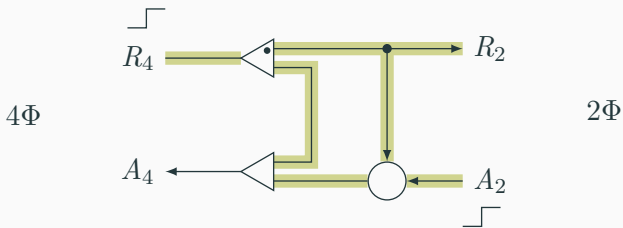
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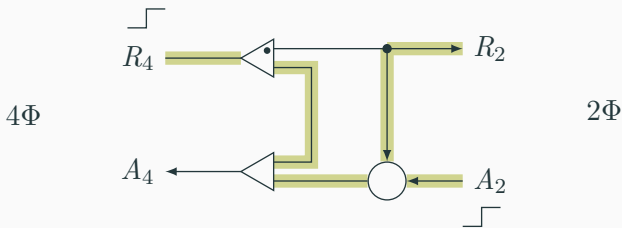
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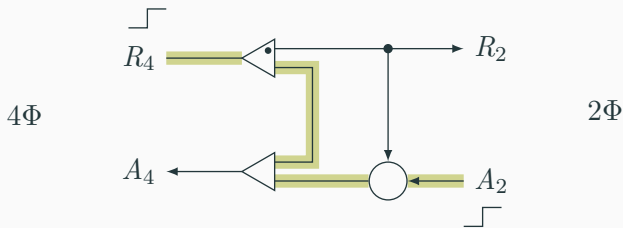
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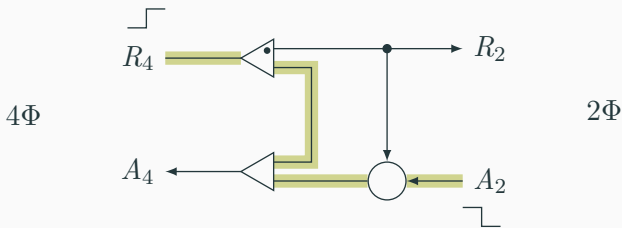
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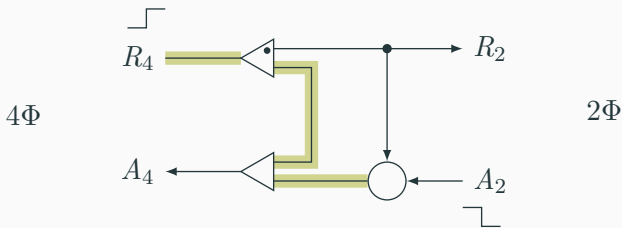
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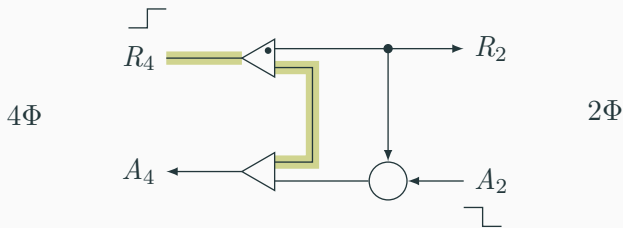
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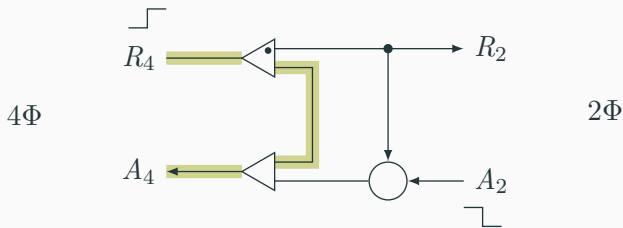
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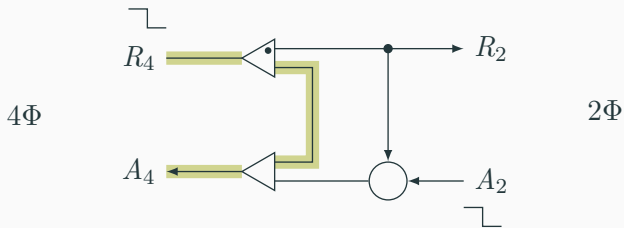
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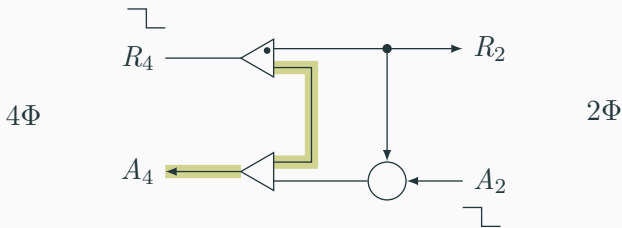
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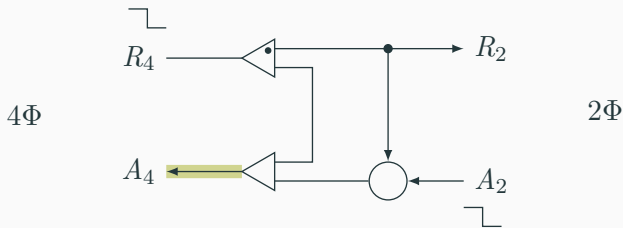
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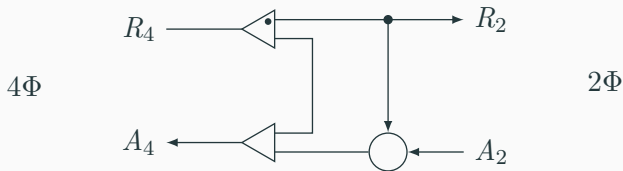
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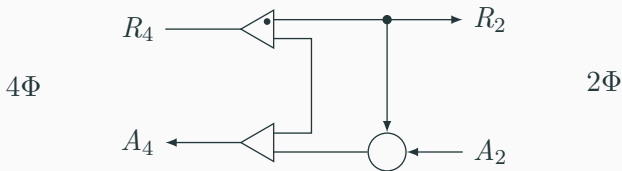
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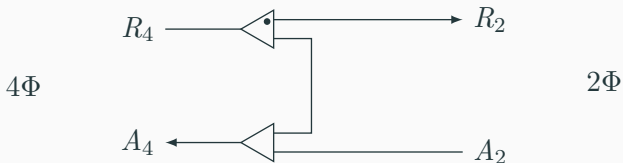
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equivalent without the FORK and JOIN ?

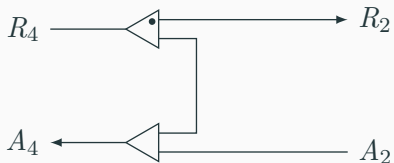
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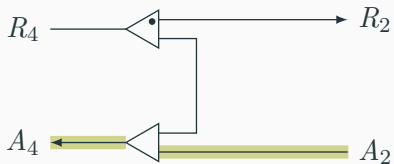


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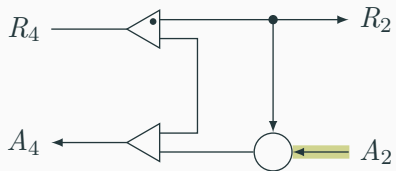
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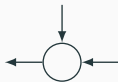
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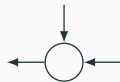
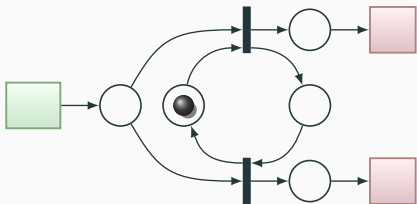
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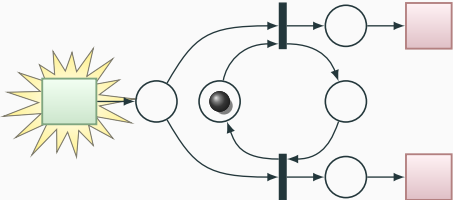
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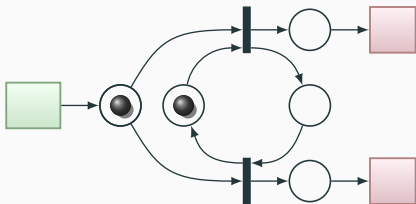
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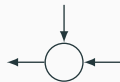
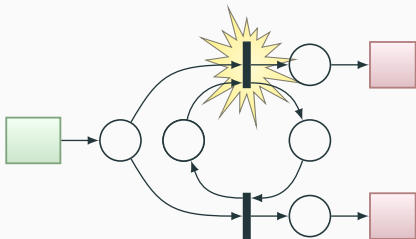
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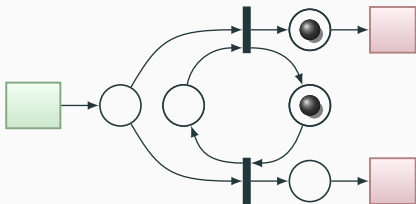
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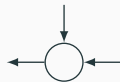
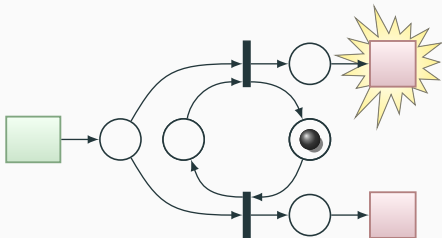
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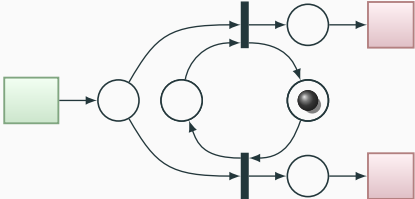
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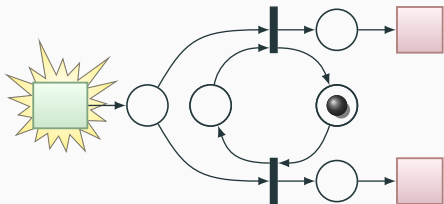
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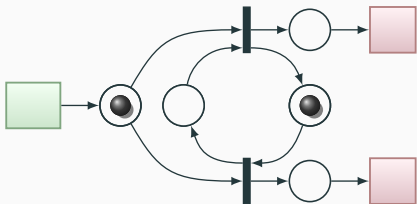
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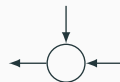
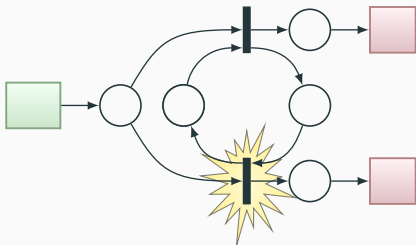
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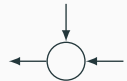
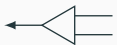
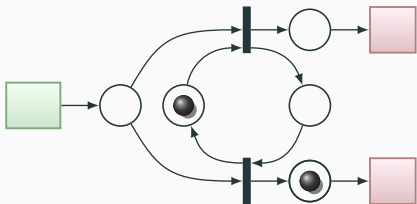
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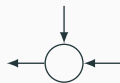
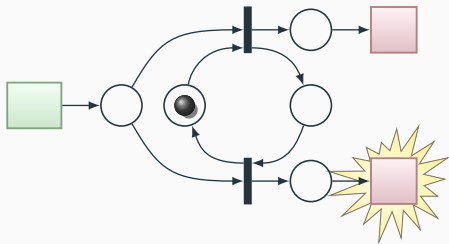
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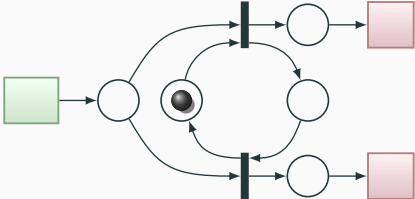
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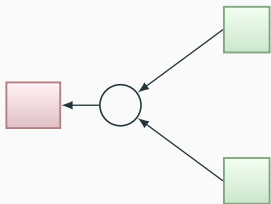
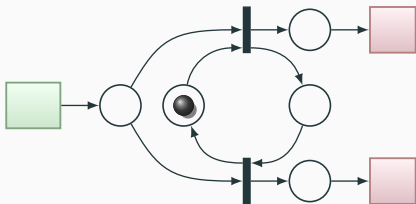
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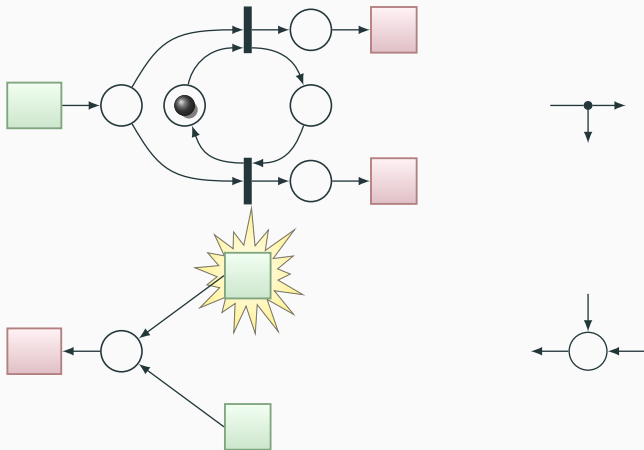
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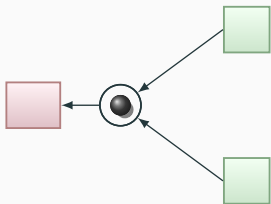
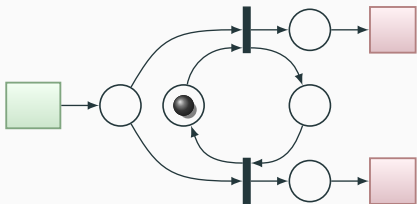
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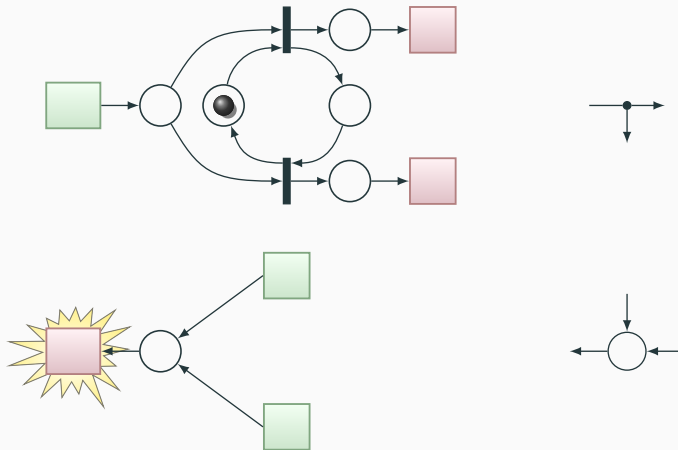
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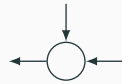
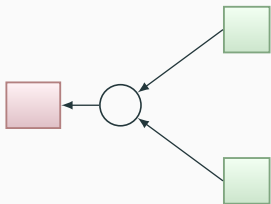
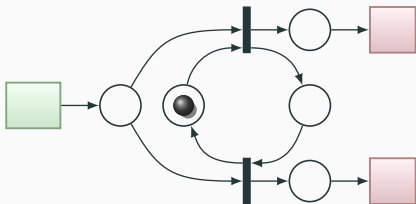
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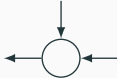
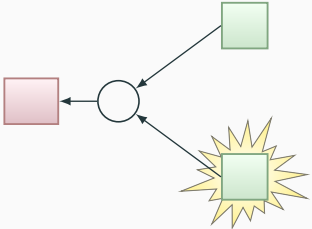
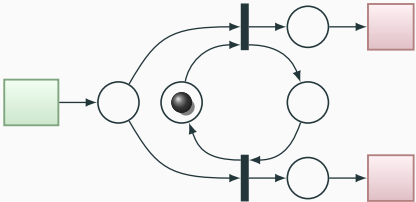
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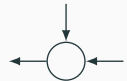
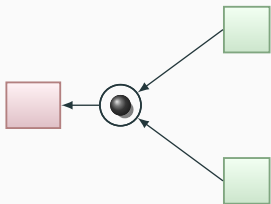
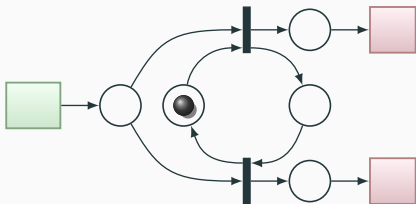
4 Φ to 2 Φ handshake converter



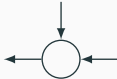
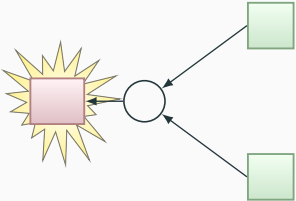
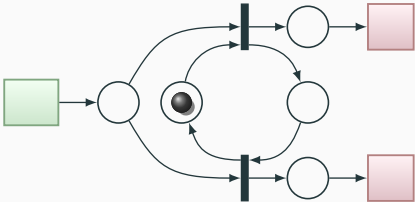
4Φ to 2Φ handshake converter



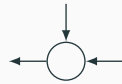
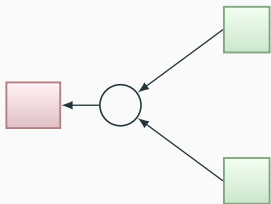
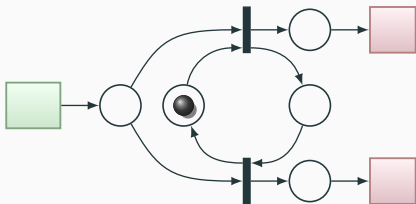
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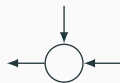
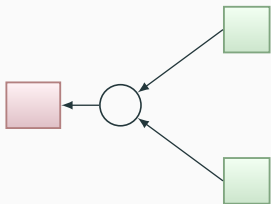
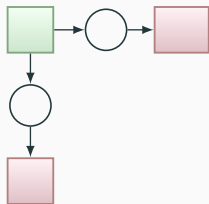
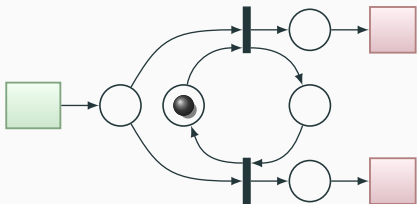
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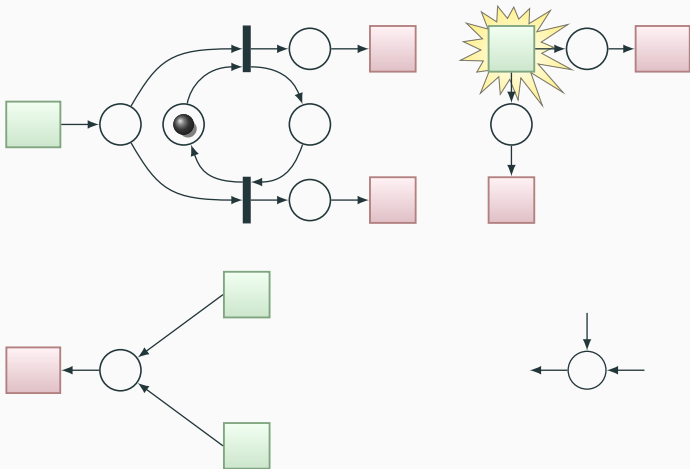
4 Φ to 2 Φ handshake converter



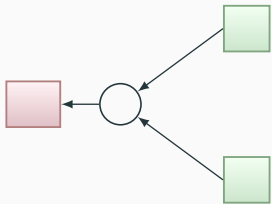
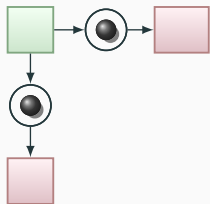
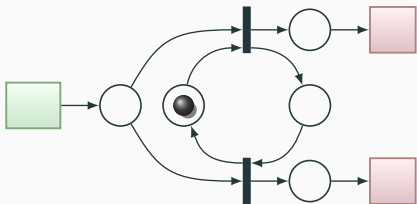
4 Φ to 2 Φ handshake converter



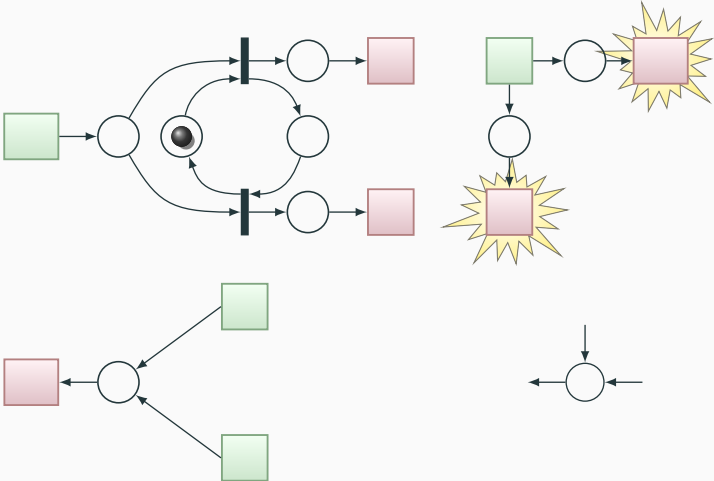
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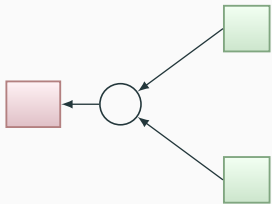
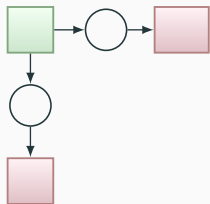
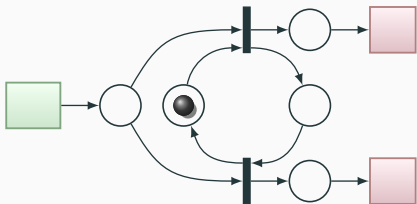
4 Φ to 2 Φ handshake converter



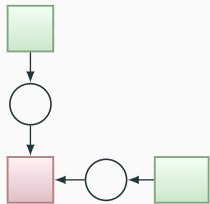
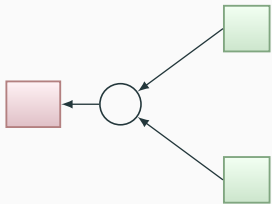
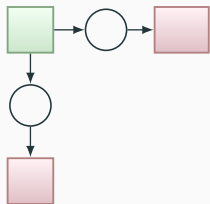
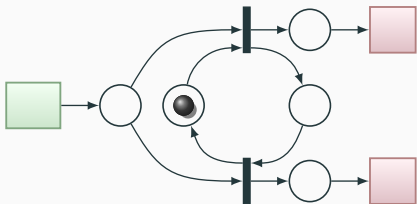
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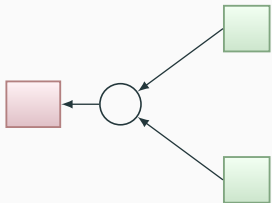
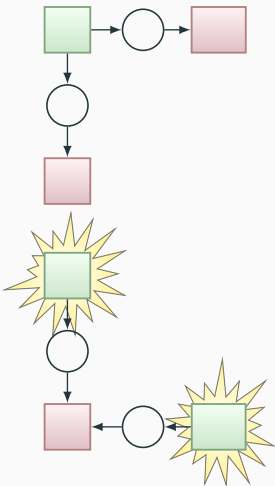
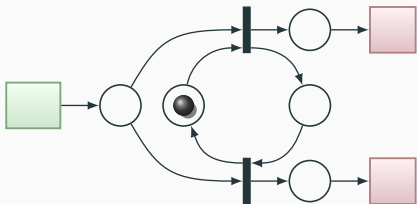
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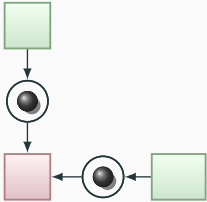
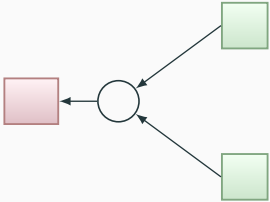
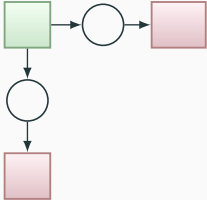
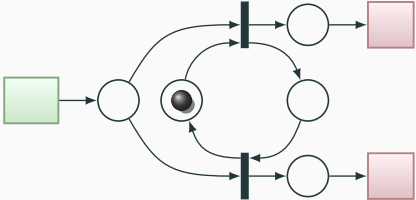
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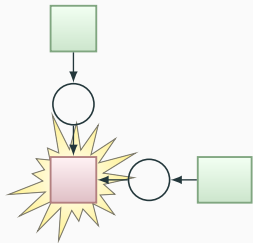
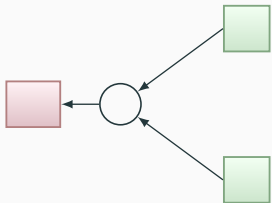
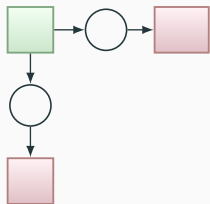
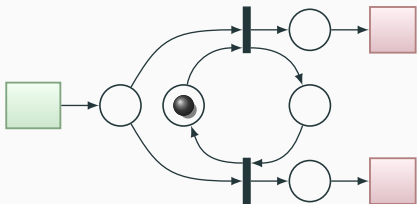
4 Φ to 2 Φ handshake converter



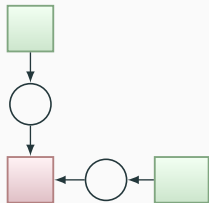
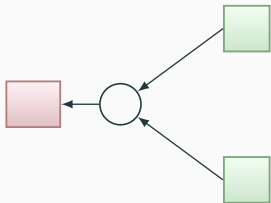
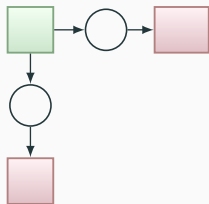
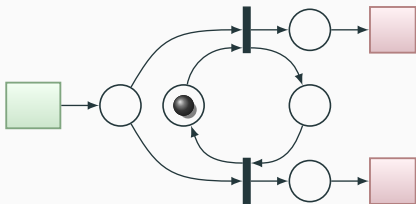
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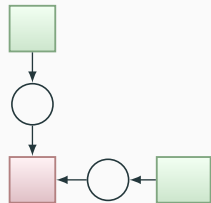
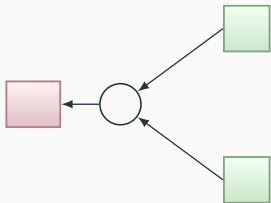
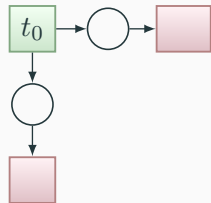
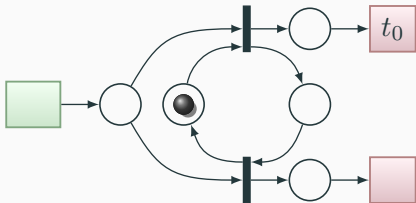
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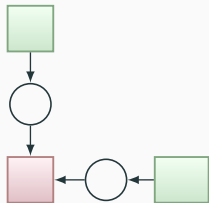
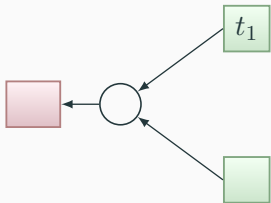
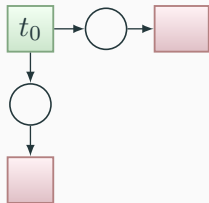
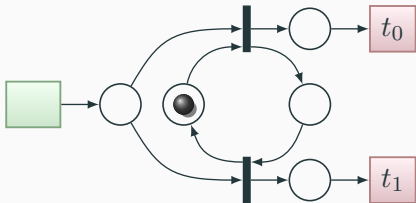
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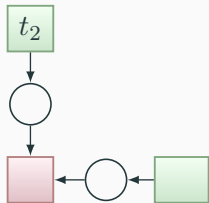
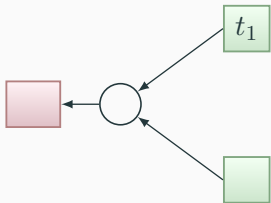
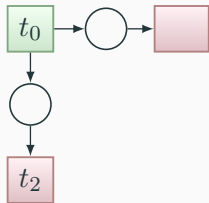
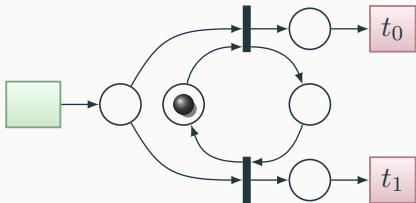
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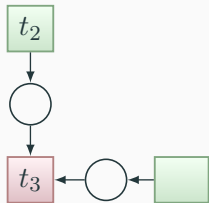
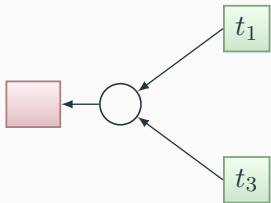
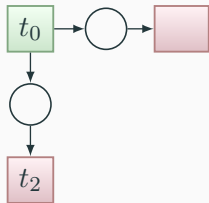
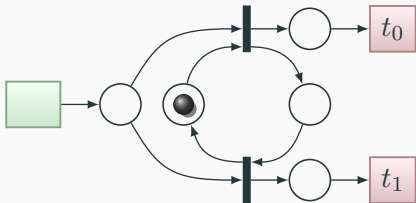
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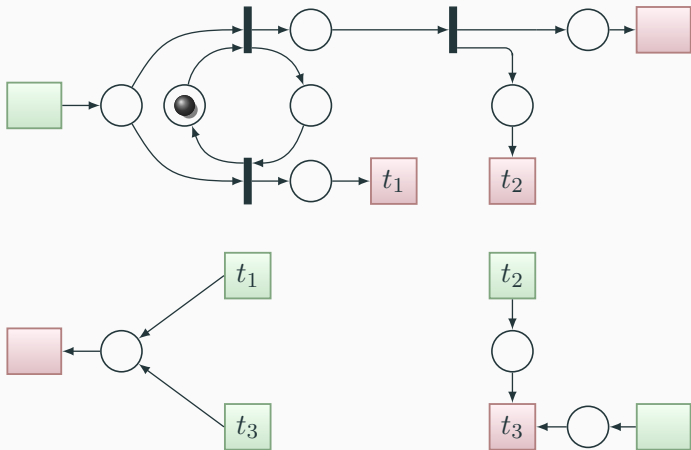
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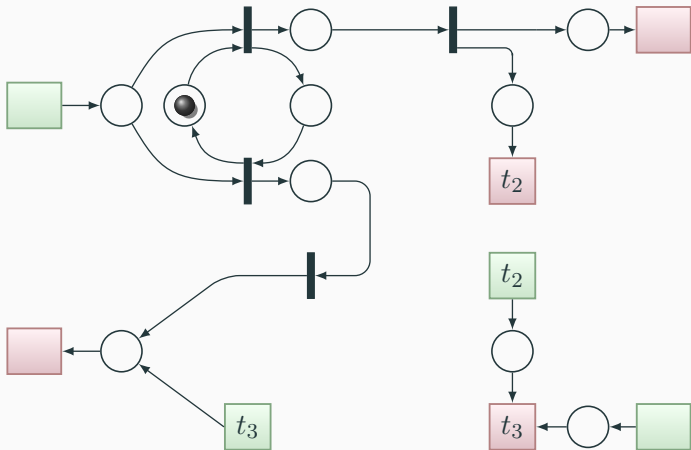
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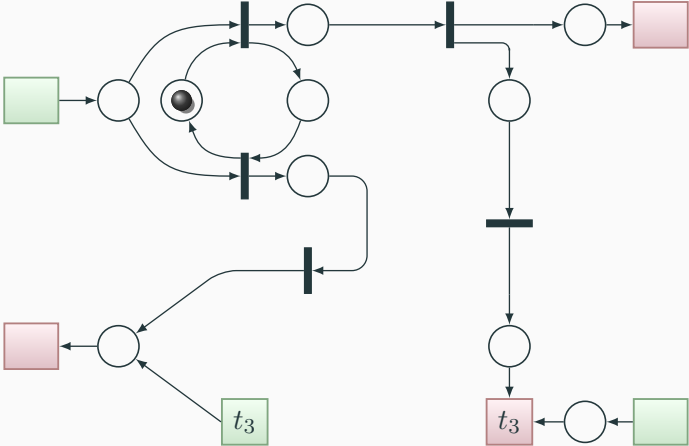
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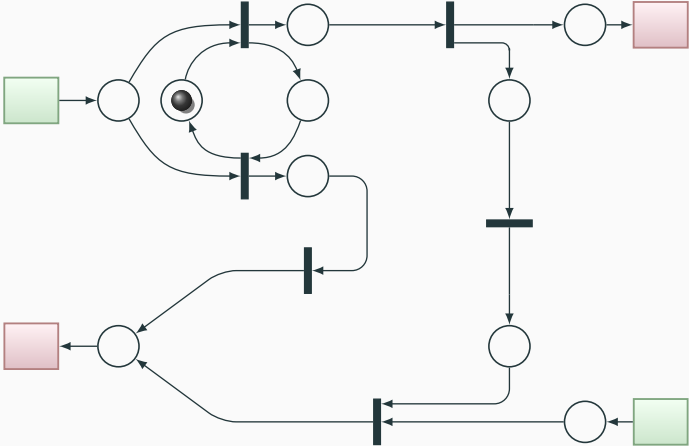
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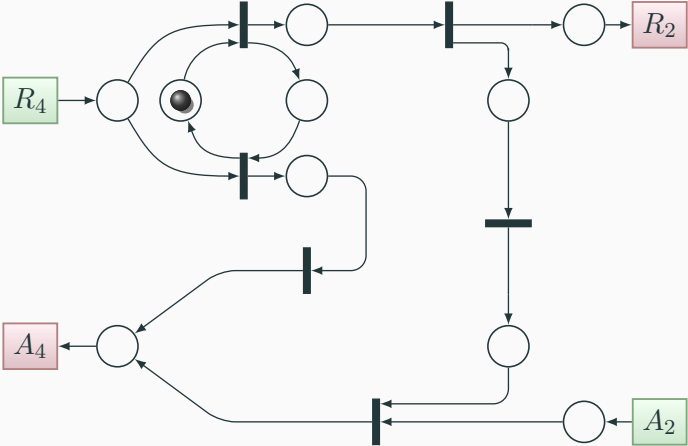
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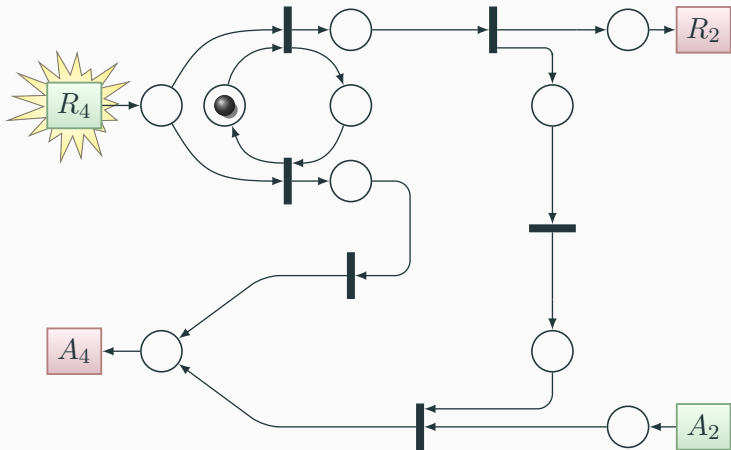
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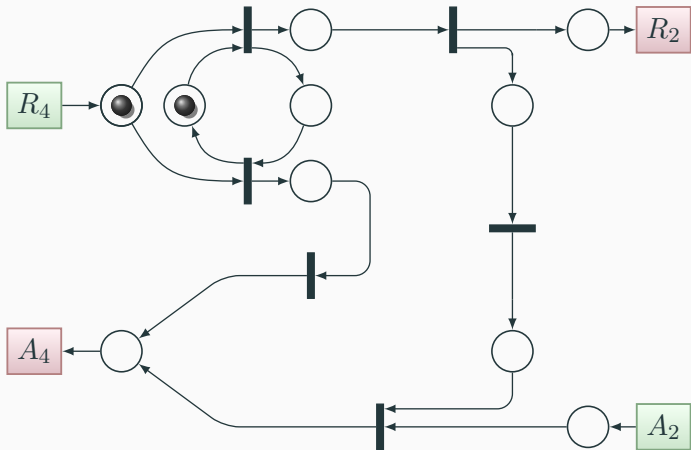
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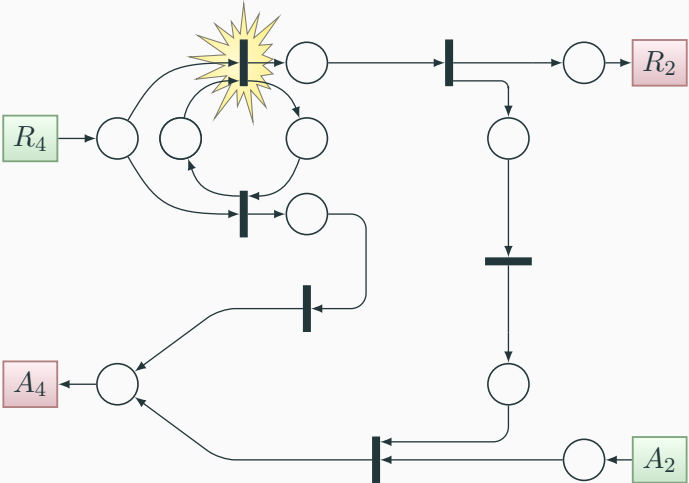
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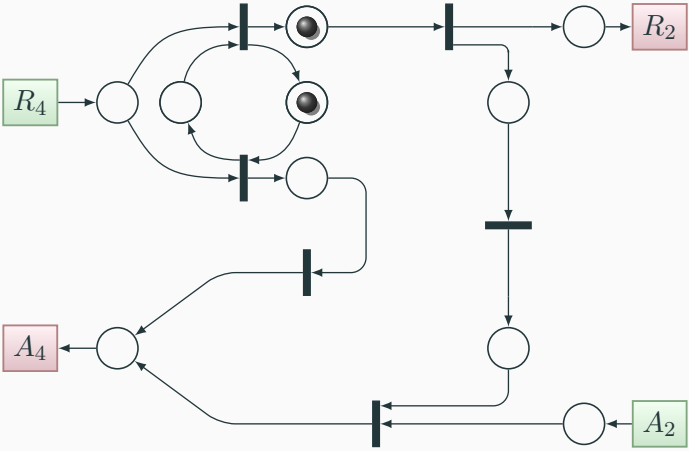
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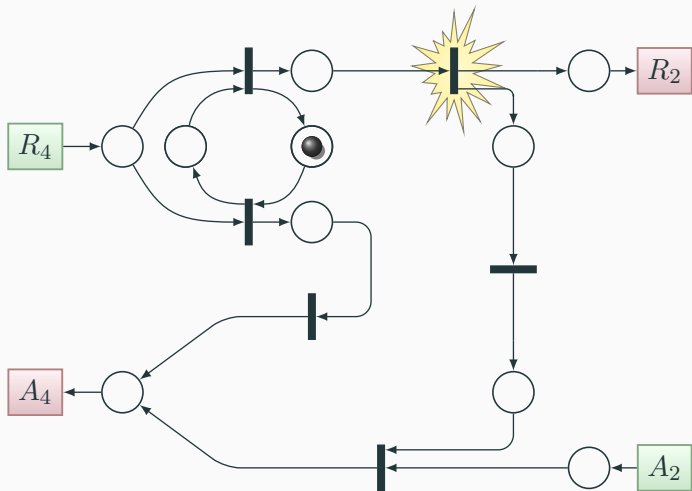
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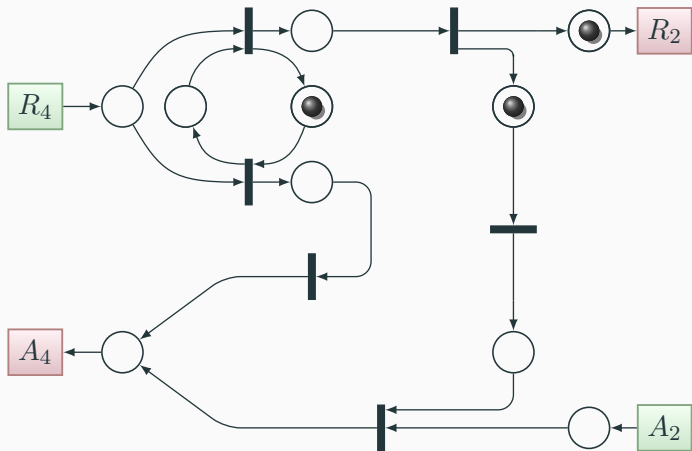
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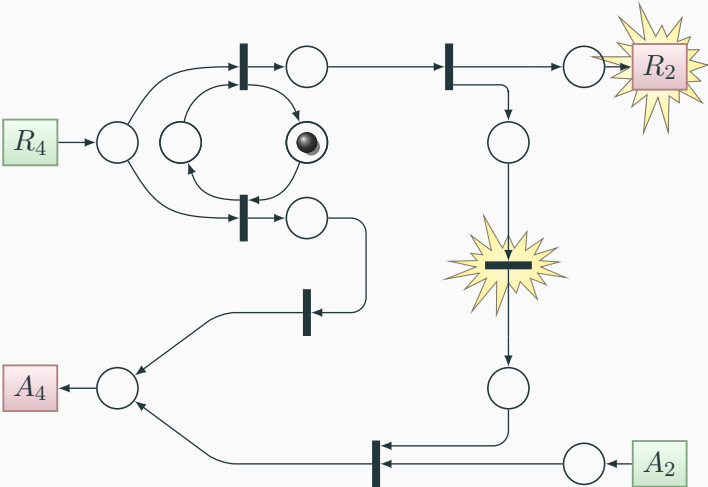
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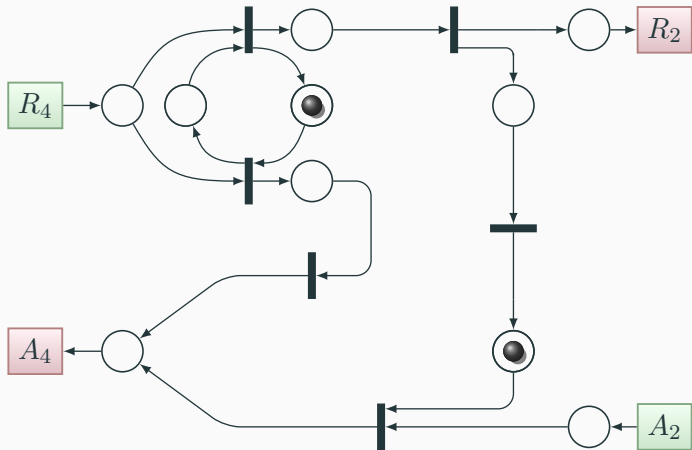
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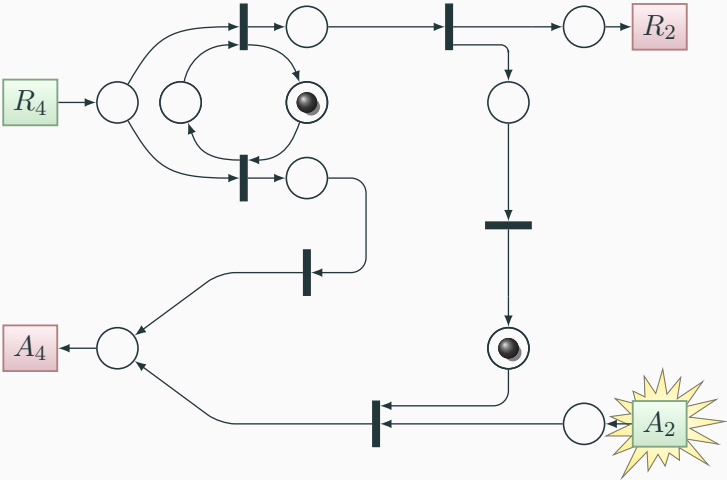
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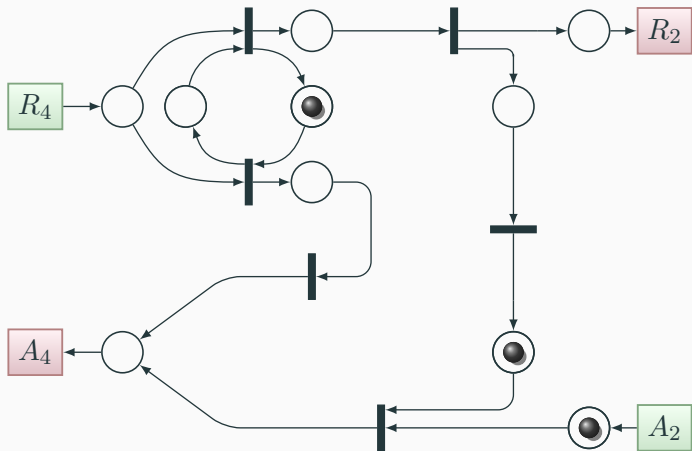
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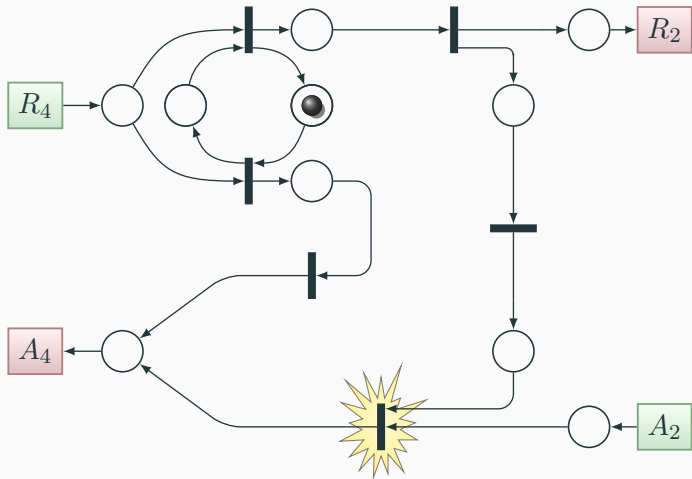
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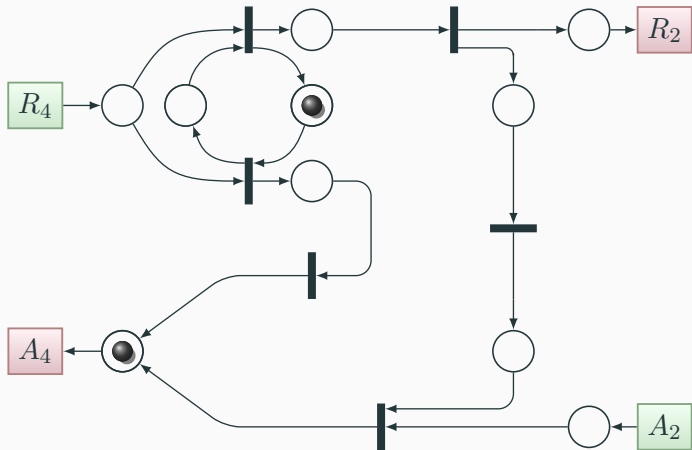
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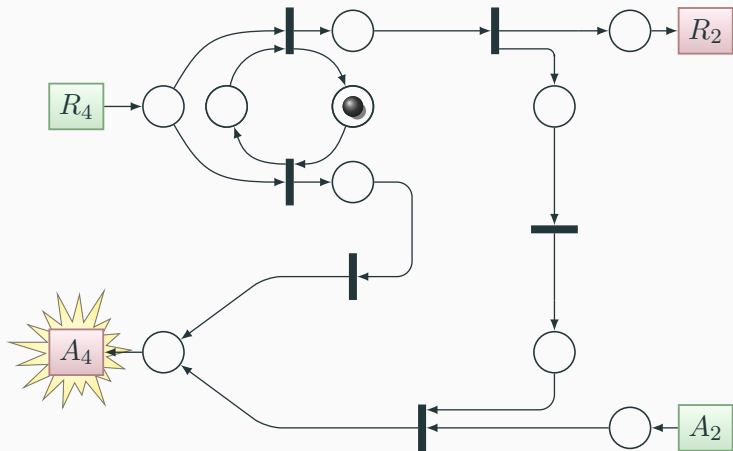
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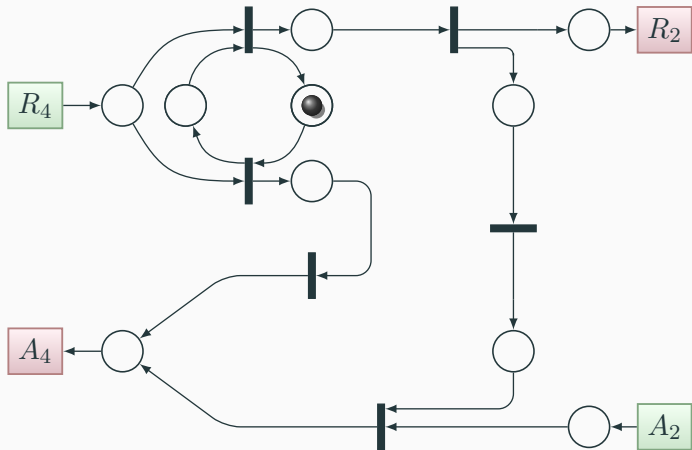
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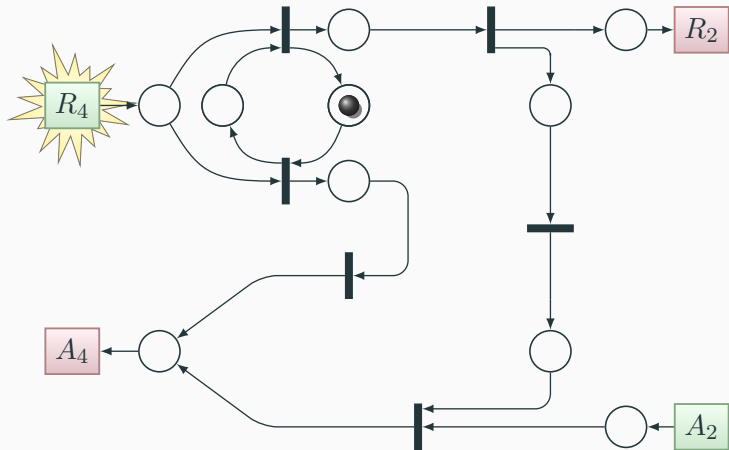
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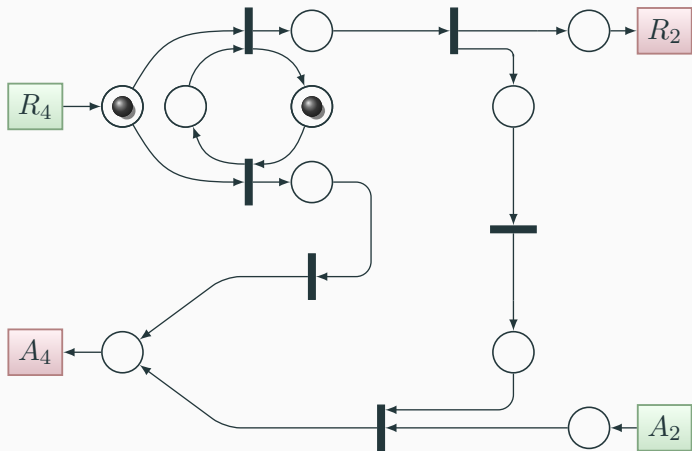
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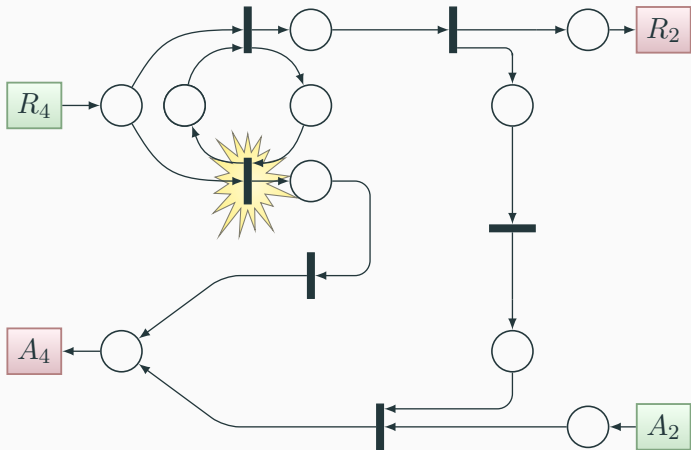
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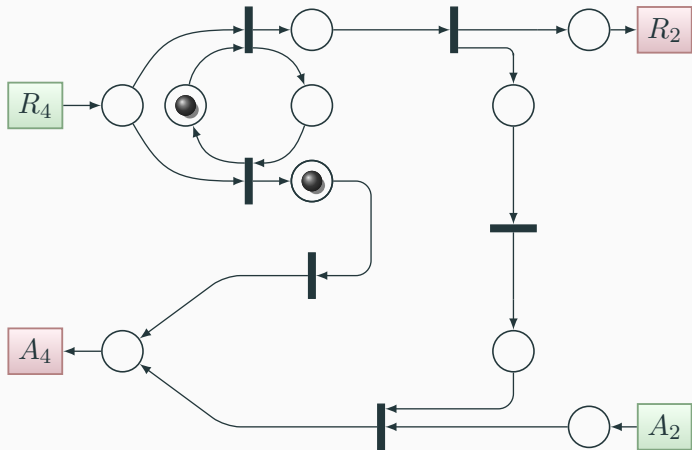
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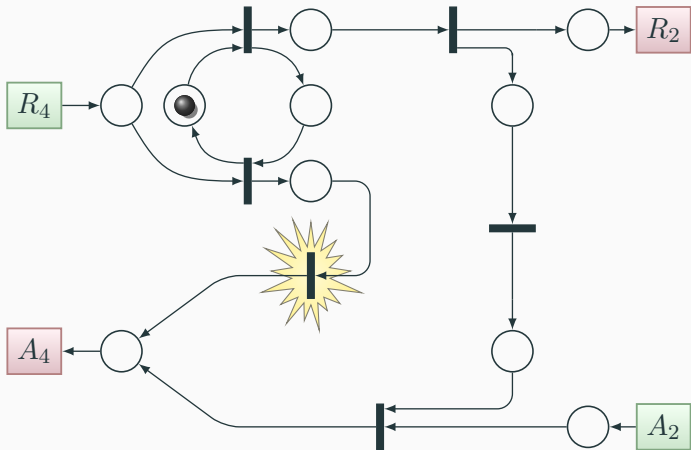
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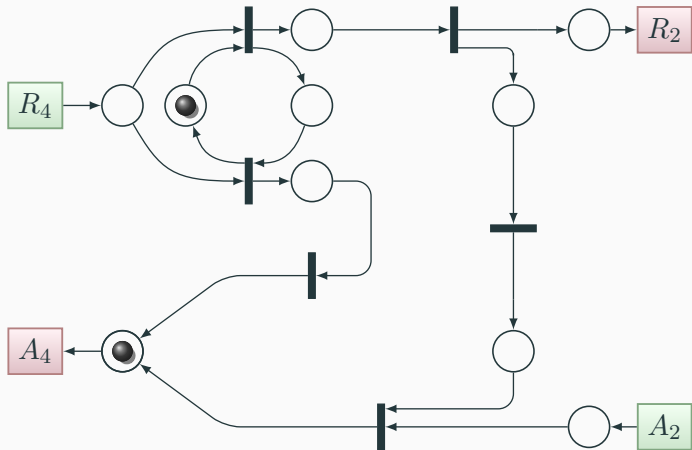
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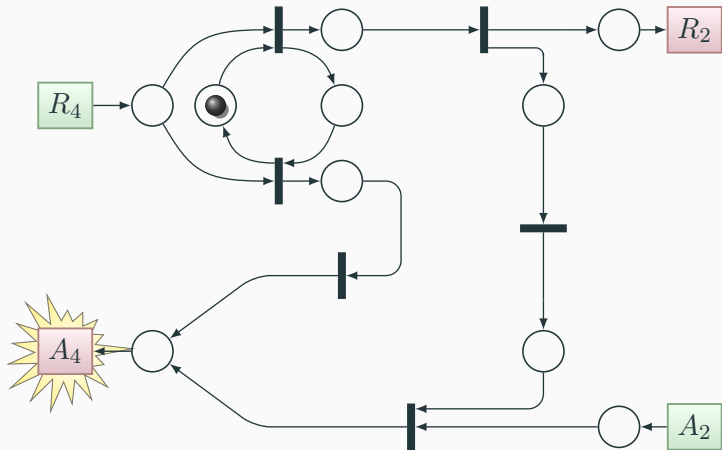
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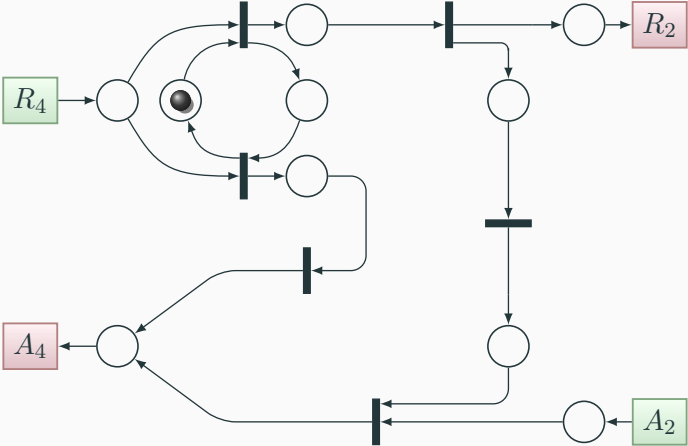
4 Φ to 2 Φ handshake converter



4 Φ to 2 Φ handshake converter



4Φ to 2Φ handshake converter



The formal approach

Give a formal account of how components

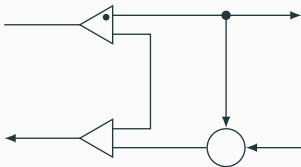
- get connected into a network
- behave individually
- behave collectively when connected into a network

Give a formal account of how components

- get connected into a network
- behave individually
- behave collectively when connected into a network

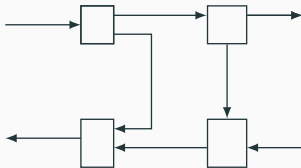
Block diagrams

Disregarding semantics, how can connections be specified ?



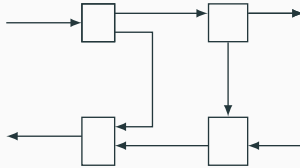
Block diagrams

Disregarding semantics, how can connections be specified ?



Block diagrams

Disregarding semantics, how can connections be specified ?

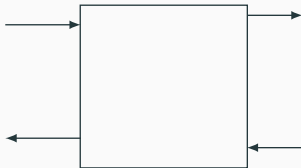


A formalism for block diagrams should enable

- algebraic description

Block diagrams

Disregarding semantics, how can connections be specified ?



A formalism for block diagrams should enable

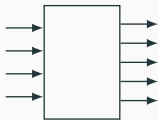
- algebraic description
- hierarchy

A *blockoid* over a set \mathbf{b} is an algebraic structure $(\mathbf{b}, \mathbf{r}, \mathbf{z}, \mathbf{i})$ with

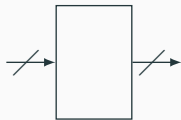
- $\mathbf{i} \in \mathbf{b}$
- $\mathbf{z} : \mathbf{b} \rightarrow \mathbf{b}$
- $\mathbf{r} : \mathbf{b} \times \mathbf{b} \rightarrow \mathbf{b}$

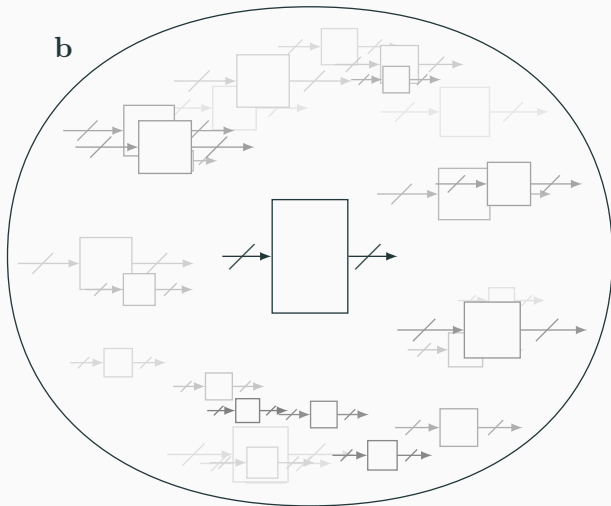
satisfying the *blockoid axioms*.

Intuition

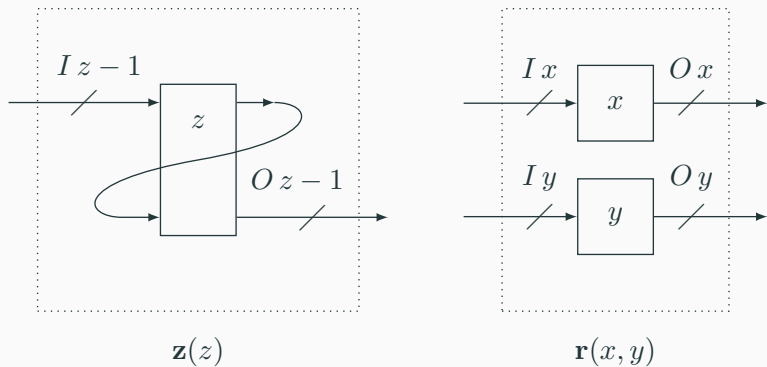


Intuition

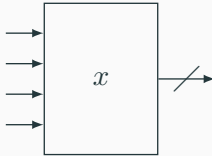




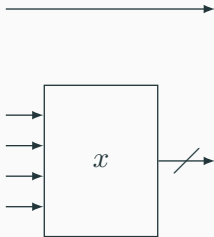
Intuition



Rolling down the inputs

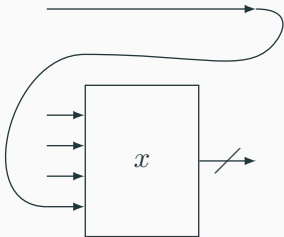


Rolling down the inputs



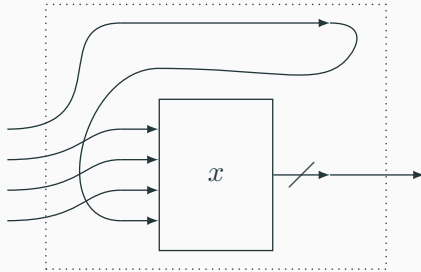
$$\mathbf{r}(\mathbf{i}, x)$$

Rolling down the inputs



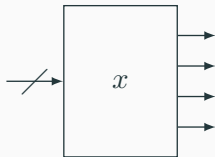
$\mathbf{zr}(\mathbf{i}, x)$

Rolling down the inputs

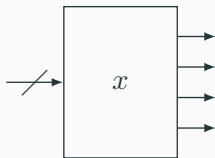


$\mathbf{zr}(\mathbf{i}, x)$

Rolling up the outputs

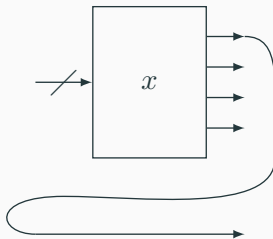


Rolling up the outputs



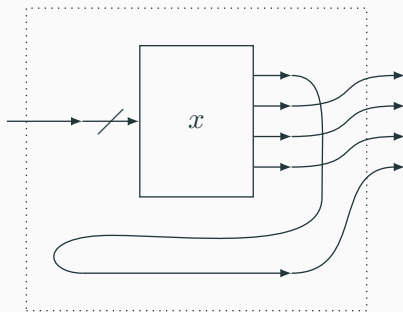
$$\mathbf{r}(x, \mathbf{i})$$

Rolling up the outputs



$\mathbf{zr}(x, \mathbf{i})$

Rolling up the outputs



$\mathbf{zr}(x, \mathbf{i})$

Blockoid axioms

There exists a *congruence* on \mathbf{b} such that

- $\forall x \in \mathbf{b}. \exists i \in \mathbb{N}. \mathbf{t} x \equiv (\mathbf{z} \circ \mathbf{t})^{i+1} \mathbf{t} x$
- $\forall x \in \mathbf{b}. \exists o \in \mathbb{N}. \mathbf{v} x \equiv (\mathbf{z} \circ \mathbf{v})^{o+1} \mathbf{v} x$

are true, where \mathbf{t} and \mathbf{v} are defined by

- $\mathbf{t} = \lambda x. \mathbf{r}(\mathbf{i}, x)$
- $\mathbf{v} = \lambda x. \mathbf{r}(x, \mathbf{i})$

The minimum $i, o \in \mathbb{N}$ satisfying these conditions for a block x are called its *input arity* $I x$ and its *output arity* $O x$.

Blockoid axioms

idempotence $\mathbf{zr}(\mathbf{i}, \mathbf{i}) \equiv \mathbf{i}$

left identity $\mathbf{r}(\mathbf{z}\mathbf{i}, x) \equiv x$

right identity $\mathbf{r}(x, \mathbf{z}\mathbf{i}) \equiv x$

associativity $\mathbf{r}(x, \mathbf{r}(y, z)) \equiv \mathbf{r}(\mathbf{r}(x, y), z)$

input arity laws $I \mathbf{z} x = I x - 1$

$$I \mathbf{r}(x, y) = I x + I y$$

output arity laws $O \mathbf{z} x = O x - 1$

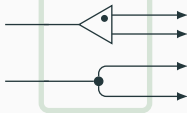
$$O \mathbf{r}(x, y) = O y + O y$$

Handshake converter blockoid

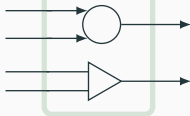


Handshake converter blockoid

$r(\text{TOGGLE, FORK})$

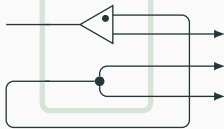


$r(\text{JOIN, MERGE})$

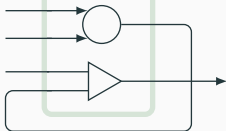


Handshake converter blockoid

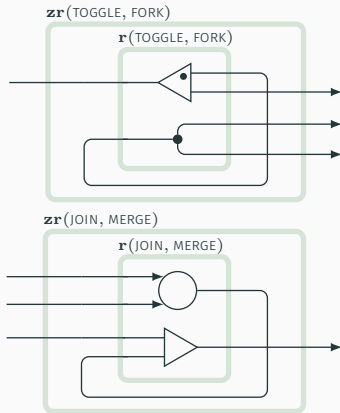
$r(\text{TOGGLE, FORK})$



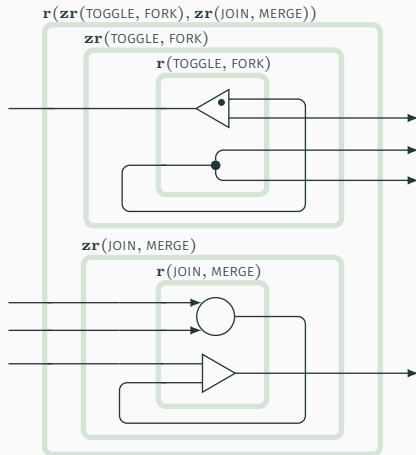
$r(\text{JOIN, MERGE})$



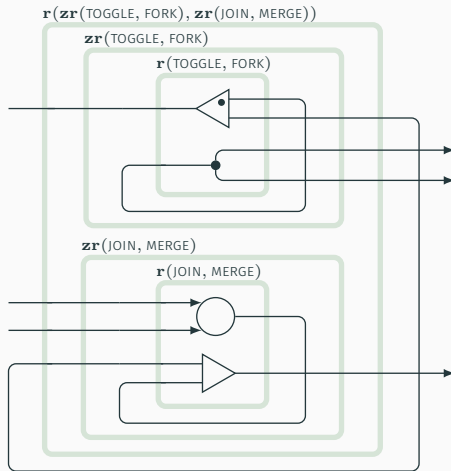
Handshake converter blockoid



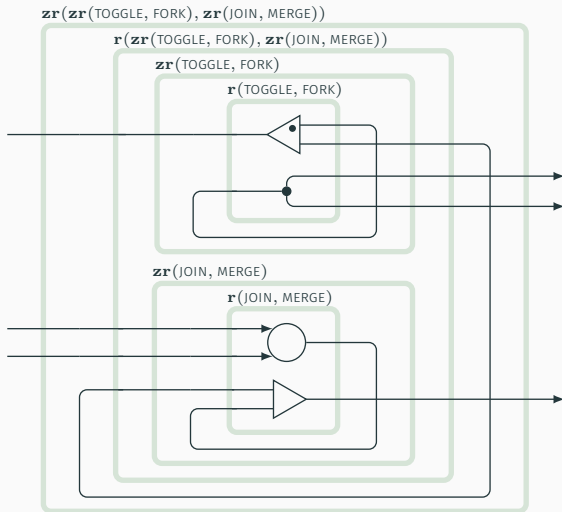
Handshake converter blockoid



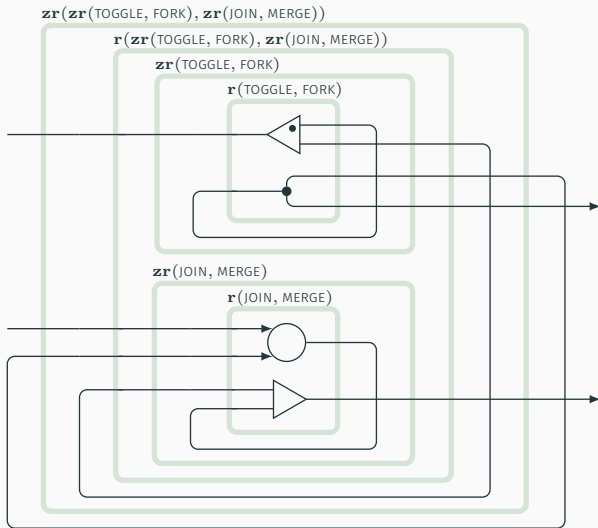
Handshake converter blockoid



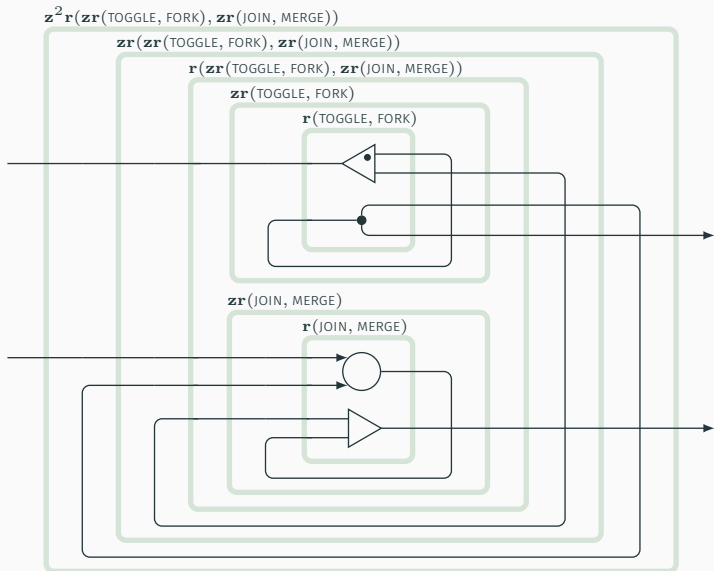
Handshake converter blockoid



Handshake converter blockoid

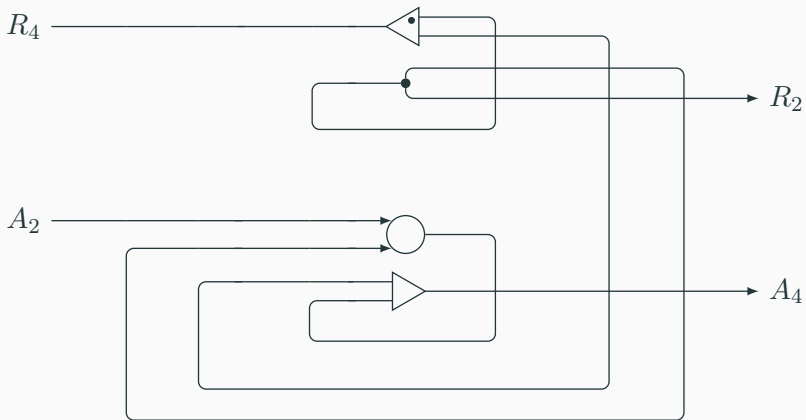


Handshake converter blockoid

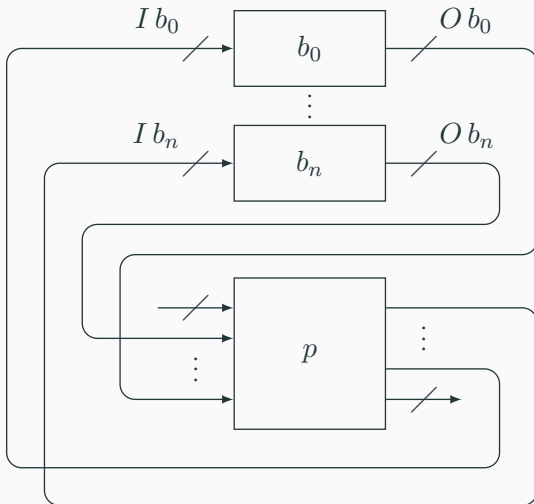


Handshake converter blockoid

$\mathbf{z}^2\mathbf{r}(\mathbf{zr}(\text{TOGGLE}, \text{FORK}), \mathbf{zr}(\text{JOIN}, \text{MERGE}))$



Universality of block combinators



Universality of block combinators

Any network of blocks $b = \langle b_0 \dots b_n \rangle$ is expressible as

- bus from p to b
 - exposed input bus
 - bus from b to p
-
- $\mathbf{z}^w \mathbf{s}^v \mathbf{z}^u (F \mathbf{r}) (b \parallel \langle p \rangle)$

$$\mathbf{t} = \lambda x. \mathbf{r}(\mathbf{i}, x)$$

$$\mathbf{s} = \mathbf{z} \circ \mathbf{t}$$

$$u = \sum_{t=0}^n O b_t$$

$$v = (I p) - u$$

$$w = \sum_{t=0}^n I b_t$$

for some *permutation network* p expressible by \mathbf{r} , \mathbf{z} and \mathbf{i} .

Universality of block combinators

Any network of blocks $b = \langle b_0 \dots b_n \rangle$ is expressible as

- bus from p to b
 - exposed input bus
 - bus from b to p
-
- $\mathbf{z}^w \mathbf{s}^v \mathbf{z}^u (F \mathbf{r}) (b \parallel \langle p \rangle)$
- fold*
- concatenate*

$$\mathbf{t} = \lambda x. \mathbf{r}(\mathbf{i}, x)$$

$$\mathbf{s} = \mathbf{z} \circ \mathbf{t}$$

$$u = \sum_{t=0}^n O b_t$$

$$v = (I p) - u$$

$$w = \sum_{t=0}^n I b_t$$

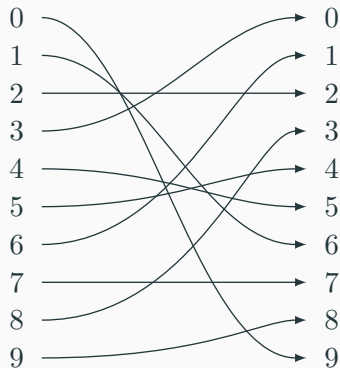
for some *permutation network* p expressible by \mathbf{r} , \mathbf{z} and \mathbf{i} .

Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\mathbf{p}(x)$

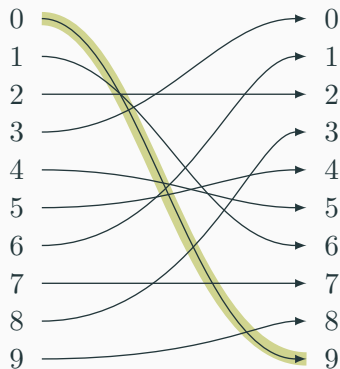


Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\mathbf{p}(x)$

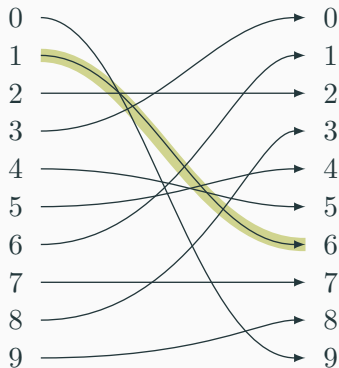


Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\mathbf{p}(x)$

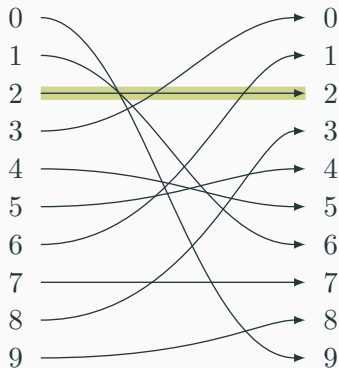


Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\mathbf{p}(x)$



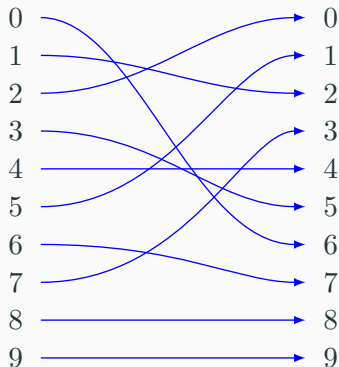
Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\langle 6, 2, 0, 5, 4, 1, 7, 3, 8, 9 \rangle$

$\mathbf{p}(x)$



Permutation networks

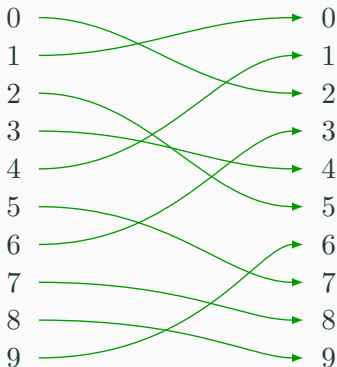
x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\langle 6, 2, 0, 5, 4, 1, 7, 3, 8, 9 \rangle$

$\langle 2, 0, 5, 4, 1, 7, 3, 8, 9, 6 \rangle$

$\mathbf{p}(x)$



Permutation networks

x

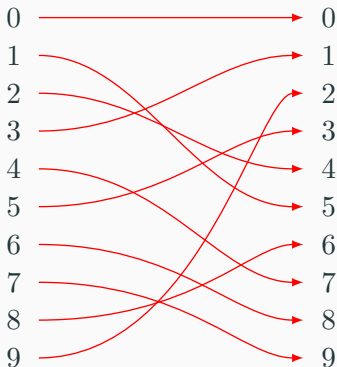
$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\langle 6, 2, 0, 5, 4, 1, 7, 3, 8, 9 \rangle$

$\langle 2, 0, 5, 4, 1, 7, 3, 8, 9, 6 \rangle$

$\langle 0, 5, 4, 1, 7, 3, 8, 9, 6, 2 \rangle$

$\mathbf{p}(x)$



Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\langle 6, 2, 0, 5, 4, 1, 7, 3, 8, 9 \rangle$

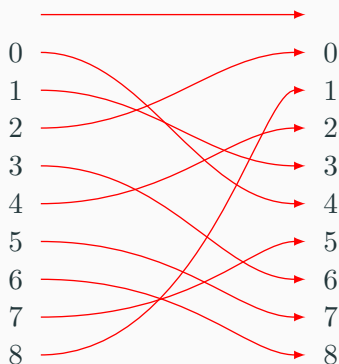
$\langle 2, 0, 5, 4, 1, 7, 3, 8, 9, 6 \rangle$

$\langle 0, 5, 4, 1, 7, 3, 8, 9, 6, 2 \rangle$

$\Downarrow -1$

$x' = \langle 4, 3, 0, 6, 2, 7, 8, 5, 1 \rangle$

$\mathbf{p}(x) = \mathbf{r}(\mathbf{i}, \mathbf{p}(x'))$



Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\langle 6, 2, 0, 5, 4, 1, 7, 3, 8, 9 \rangle$

$\langle 2, 0, 5, 4, 1, 7, 3, 8, 9, 6 \rangle$

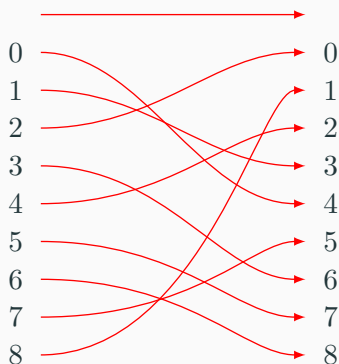
$\langle 0, 5, 4, 1, 7, 3, 8, 9, 6, 2 \rangle$

$\Downarrow -1$

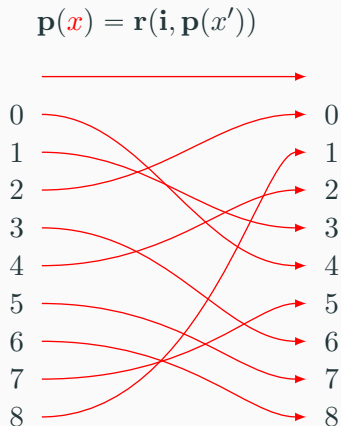
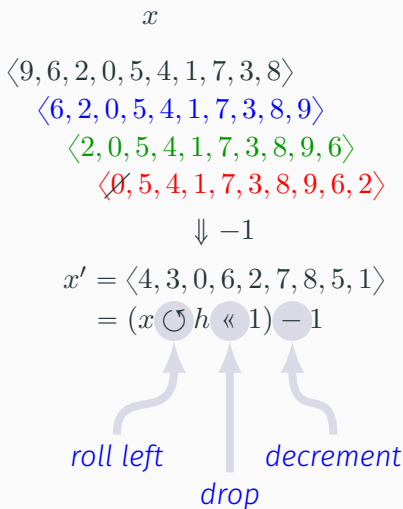
$x' = \langle 4, 3, 0, 6, 2, 7, 8, 5, 1 \rangle$

$= (x \circlearrowleft h \ll 1) - 1$

$\mathbf{p}(x) = \mathbf{r}(\mathbf{i}, \mathbf{p}(x'))$



Permutation networks



Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\langle 6, 2, 0, 5, 4, 1, 7, 3, 8, 9 \rangle$

$\langle 2, 0, 5, 4, 1, 7, 3, 8, 9, 6 \rangle$

$\langle 0, 5, 4, 1, 7, 3, 8, 9, 6, 2 \rangle$

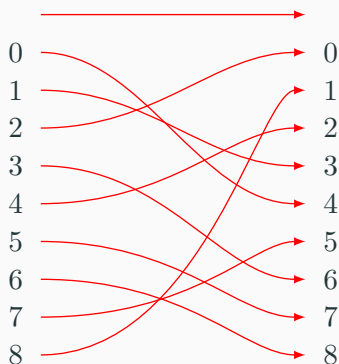
$\Downarrow -1$

$x' = \langle 4, 3, 0, 6, 2, 7, 8, 5, 1 \rangle$

$= (x \circlearrowleft h \ll 1) - 1$

where $h = (x^{-1})_0$

$\mathbf{p}(x) = \mathbf{r}(\mathbf{i}, \mathbf{p}(x'))$



Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\langle 6, 2, 0, 5, 4, 1, 7, 3, 8, 9 \rangle$

$\langle 2, 0, 5, 4, 1, 7, 3, 8, 9, 6 \rangle$

$\langle 0, 5, 4, 1, 7, 3, 8, 9, 6, 2 \rangle$

$\Downarrow -1$

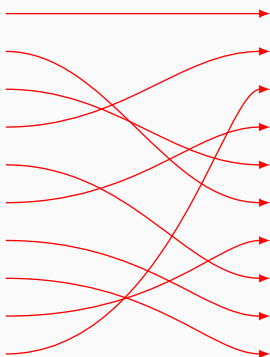
$x' = \langle 4, 3, 0, 6, 2, 7, 8, 5, 1 \rangle$

$= (x \circlearrowleft h \ll 1) - 1$

where $h = (x^{-1})_0$

$\mathbf{t} = \lambda a. \mathbf{r}(\mathbf{i}, a)$

$\mathbf{p}(x) = \mathbf{r}(\mathbf{i}, \mathbf{p}(x'))$



Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\langle 6, 2, 0, 5, 4, 1, 7, 3, 8, 9 \rangle$

$\langle 2, 0, 5, 4, 1, 7, 3, 8, 9, 6 \rangle$

$\langle 0, 5, 4, 1, 7, 3, 8, 9, 6, 2 \rangle$

$\Downarrow -1$

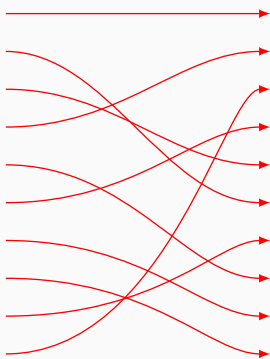
$x' = \langle 4, 3, 0, 6, 2, 7, 8, 5, 1 \rangle$

$= (x \cup h \ll 1) - 1$

where $h = (x^{-1})_0$

$\mathbf{t} = \lambda a. \mathbf{r}(\mathbf{i}, a)$

$\mathbf{p}(x) = \mathbf{tp}(x')$



Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\langle 6, 2, 0, 5, 4, 1, 7, 3, 8, 9 \rangle$

$\langle 2, 0, 5, 4, 1, 7, 3, 8, 9, 6 \rangle$

$\langle 0, 5, 4, 1, 7, 3, 8, 9, 6, 2 \rangle$

$\Downarrow -1$

$x' = \langle 4, 3, 0, 6, 2, 7, 8, 5, 1 \rangle$

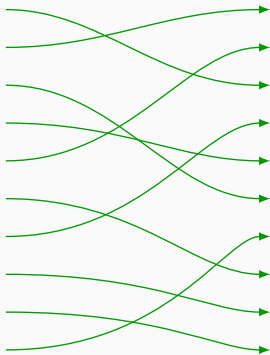
$= (x \cup h \ll 1) - 1$

where $h = (x^{-1})_0$

$\mathbf{t} = \lambda a. \mathbf{r}(\mathbf{i}, a)$

$\mathbf{s} = \mathbf{z} \circ \mathbf{t}$

$\mathbf{p}(x) = \mathbf{stp}(x')$



Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\langle 6, 2, 0, 5, 4, 1, 7, 3, 8, 9 \rangle$

$\langle 2, 0, 5, 4, 1, 7, 3, 8, 9, 6 \rangle$

$\langle 0, 5, 4, 1, 7, 3, 8, 9, 6, 2 \rangle$

$\Downarrow -1$

$x' = \langle 4, 3, 0, 6, 2, 7, 8, 5, 1 \rangle$

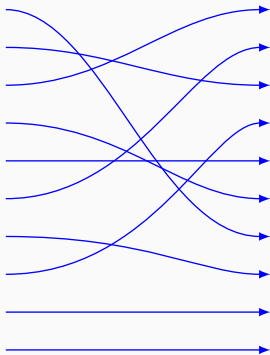
$= (x \circlearrowleft h \ll 1) - 1$

where $h = (x^{-1})_0$

$\mathbf{t} = \lambda a. \mathbf{r}(\mathbf{i}, a)$

$\mathbf{s} = \mathbf{z} \circ \mathbf{t}$

$\mathbf{p}(x) = \mathbf{s}^2 \mathbf{t} \mathbf{p}(x')$



Permutation networks

x

$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$

$\langle 6, 2, 0, 5, 4, 1, 7, 3, 8, 9 \rangle$

$\langle 2, 0, 5, 4, 1, 7, 3, 8, 9, 6 \rangle$

$\langle 0, 5, 4, 1, 7, 3, 8, 9, 6, 2 \rangle$

$\Downarrow -1$

$x' = \langle 4, 3, 0, 6, 2, 7, 8, 5, 1 \rangle$

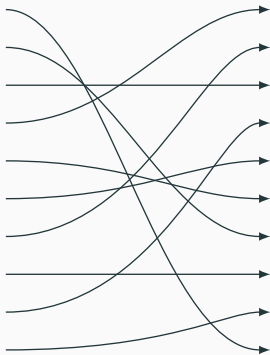
$= (x \circlearrowleft h \ll 1) - 1$

where $h = (x^{-1})_0$

$\mathbf{t} = \lambda a. \mathbf{r}(\mathbf{i}, a)$

$\mathbf{s} = \mathbf{z} \circ \mathbf{t}$

$\mathbf{p}(x) = \mathbf{s}^h \mathbf{t} \mathbf{p}(x')$



Permutation network combinator definition

The permutation obtained from $x \in \mathbb{N}^*$ rolled left by

$$h = (x^{-1})_0$$

decapitated and pointwise decremented

$$x' = (x \circlearrowleft h \ll 1) - 1$$

implies the recurrence

$$\mathbf{p}(x) = \begin{cases} \mathbf{i} & \text{if } |x| = 1 \\ (\lambda h. \mathbf{s}^h \mathbf{t} \mathbf{p}((x \circlearrowleft h \ll 1) - 1)) (x^{-1})_0 & \text{if } |x| > 1 \end{cases}$$

implying universality of \mathbf{r} , \mathbf{z} , and \mathbf{i} for block diagrams !

Permutation network examples

x	$\mathbf{p}(x)$
$\langle 9, 6, 2, 0, 5, 4, 1, 7, 3, 8 \rangle$	$\mathbf{s^3ts^2ts^5ts^3ts^4ts^4ts^3t^3i}$
$\langle 5, 0, 9, 3, 1, 8, 7, 2, 6, 4 \rangle$	$\mathbf{sts^2ts^2ts^4ts^3t^2s^3ts^2tsti}$
$\langle 4, 5, 1, 7, 3, 2, 8, 0, 9, 6 \rangle$	$\mathbf{s^7ts^4ts^2ts^6ts^3t^2s^3t^3i}$
$\langle 1, 3, 2, 7, 8, 6, 9, 0, 4, 5 \rangle$	$\mathbf{s^7ts^2tsts^6ts^4t^2s^2tst^2i}$
$\langle 2, 9, 6, 0, 1, 5, 3, 7, 8, 4 \rangle$	$\mathbf{s^3t^2s^5ts^3ts^2ts^2ts^3t^3i}$
$\langle 8, 3, 7, 6, 0, 1, 9, 5, 2, 4 \rangle$	$\mathbf{s^4t^2s^2ts^2ts^4ts^4ts^2ts^2tsti}$
$\langle 8, 9, 7, 0, 5, 2, 4, 1, 6, 3 \rangle$	$\mathbf{s^3ts^3ts^6ts^2ts^4ts^4t^2s^2t^2i}$
$\langle 0, 9, 2, 7, 1, 4, 3, 8, 5, 6 \rangle$	$\mathbf{ts^3ts^6ts^2ts^5tst^2st^2i}$
$\langle 5, 3, 4, 8, 1, 7, 0, 6, 2, 9 \rangle$	$\mathbf{s^6ts^7ts^2ts^2t^2s^4ts^2ts^2tsti}$
$\langle 0, 6, 2, 4, 8, 5, 3, 7, 9, 1 \rangle$	$\mathbf{ts^8tsts^3ts^3tsts^2tststi}$
$\langle 3, 0, 7, 9, 1, 8, 6, 2, 5, 4 \rangle$	$\mathbf{sts^2ts^2ts^2ts^5ts^4ts^3t^2sti}$

Give a formal account of how components

- ✓ get connected into a network
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Component models – first attempt

Model the components as Petri nets.

- \mathbb{T} universe of observable transitions
- \mathbb{V} universe of places and unobservable transitions
- \mathbb{P} Petri nets (P, T, A, M, F)

- places $P \subset \mathbb{V}$
- transitions $T \subset \mathbb{T} \cup \mathbb{V}$
- arcs $A \subseteq (P \times T) \cup (T \times P)$
- initial marking $M \subseteq P$
- final marking $F \subseteq P$

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but then inputs and outputs are indistinguishable

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- \mathbb{T} universe of observable transitions
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- \mathbb{D} delay insensitive processes $p \in \mathcal{P}(\mathbb{T}) \times \mathcal{P}(\mathbb{T}) \times \mathbb{P}$

For $p = (I, O, N) \in \mathbb{D}$

- Petri net $N = (P, T, A, M, F) \in \mathbb{P}$
- input alphabet $I \supseteq T \cap \mathbb{T}$
- output alphabet $O \supseteq T \cap \mathbb{T}$

Refinement over processes

From a delay insensitive process $X = (I, O, N) \in \mathbb{D}$, infer

- a *reachability graph* $\mathbf{RG}(X)$ from the Petri net N .

From the reachability graph, infer

- a *quiescent trace recognizing automaton* $\mathbf{QR}(X)$
- a *divergent trace recognizing automaton* $\mathbf{DR}(X)$

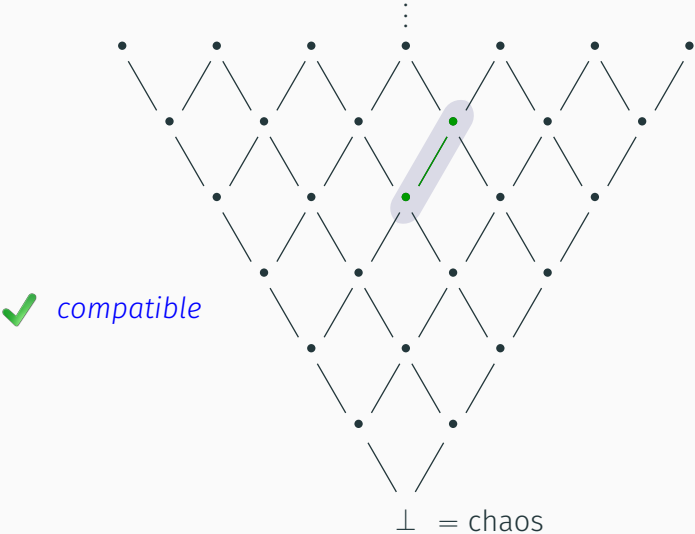
From their languages $\mathcal{L} \mathbf{QR}(X), \mathcal{L} \mathbf{DR}(X) \in (I \cup O)^*$, infer

- a *relational trace set* $\llbracket X \rrbracket = \mathcal{L} \mathbf{QR}(X) \cup \mathcal{L} \mathbf{DR}(X)$

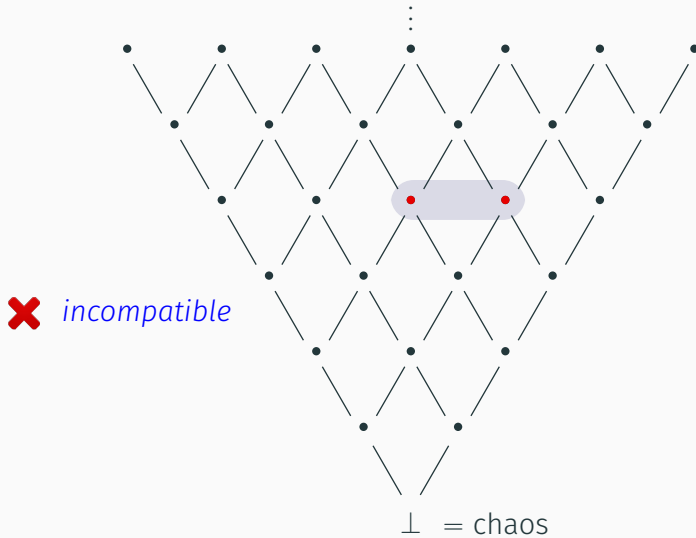
such that the *refinement* relation $X \sqsubseteq Y$ coincides with

$$\llbracket X \rrbracket \supseteq \llbracket Y \rrbracket.$$

Complete partial ordering by refinement



Complete partial ordering by refinement



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but then name clashes among transitions are inconvenient

Component models – third attempt

Model the components as blocks with terminals.

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- \mathbb{V} universe of places and unobservable transitions
- \mathbb{P} Petri nets (P, T, A, M, F)
- \mathbb{D} delay insensitive processes $p \in \mathcal{P}(\mathbb{T}) \times \mathcal{P}(\mathbb{T}) \times \mathbb{P}$
- \mathbb{B} primitive blocks $b \in \mathbb{N} \times \mathbb{N} \times ((\mathbb{T}^* \times \mathbb{T}^*) \rightarrow \mathbb{D})$

for $b = (I, O, B) \in \mathbb{B}$

- input arity $I \in \mathbb{N}$
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- process $(I', O', N) = B(i, o)$ has $I' = \mathcal{R}(i), O' = \mathcal{R}(o)$

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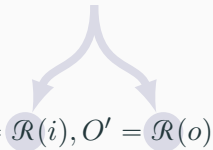
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range of a list



For a standardized infinite list $\mathbb{G} \in \mathbb{T}^*$ of generic symbols, let

$$\mathcal{J}_{\mathbb{B}\mathbb{D}} : \mathbb{B} \rightarrow \mathbb{D}$$

map components to processes by

$$\mathcal{J}_{\mathbb{B}\mathbb{D}}(I, O, B) = B(\mathbb{G} \upharpoonright I, \mathbb{G} \ll I \upharpoonright O)$$

and let refinement among components $X, Y \in \mathbb{B}$ be defined by

$$X \stackrel{\epsilon}{\sqsubseteq} Y \Leftrightarrow \mathcal{J}_{\mathbb{B}\mathbb{D}}(X) \sqsubseteq \mathcal{J}_{\mathbb{B}\mathbb{D}}(Y).$$

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map components to processes by

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list truncation

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A blockoid $(\mathbb{B}, \mathbf{R}_{\mathbb{B}}, \mathbf{Z}_{\mathbb{B}}, \mathbf{I}_{\mathbb{B}})$ over circuit components

For the universe of primitive blocks

$$\mathbb{B} = \mathbb{N} \times \mathbb{N} \times (((\mathbb{T}^* \times \mathbb{T}^*) \rightarrow \mathbb{D}))$$

let $\mathbf{I}_{\mathbb{B}} = (I, O, B_{\mathbf{I}}) \in \mathbb{B}$ have $I = O = 1$ and

$$B_{\mathbf{I}}(\langle a \rangle, \langle b \rangle) = (\{a\}, \{b\}, N)$$



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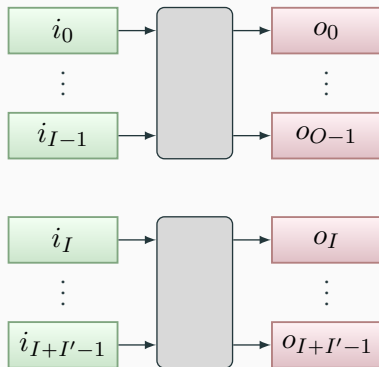
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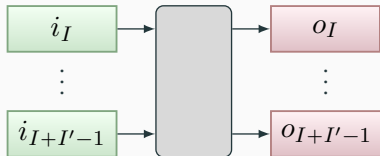
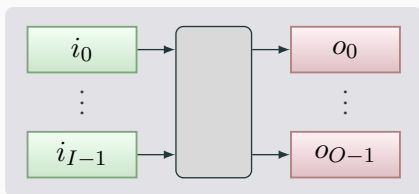


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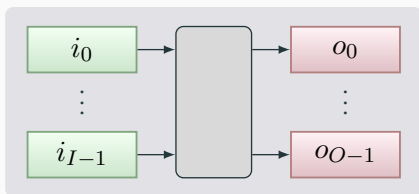


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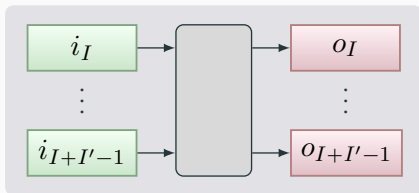
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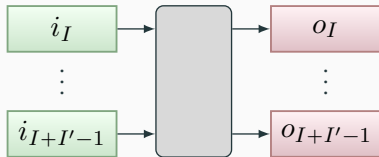
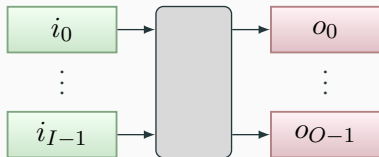


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*Parallel composition
of DI processes*

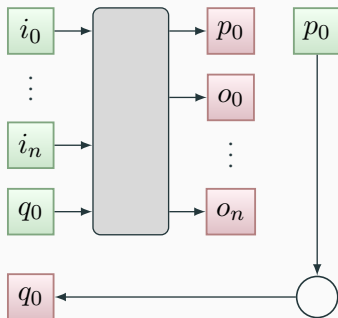


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for arbitrary distinct $p, q \in \mathbb{T}^1$ disjoint from i and o .

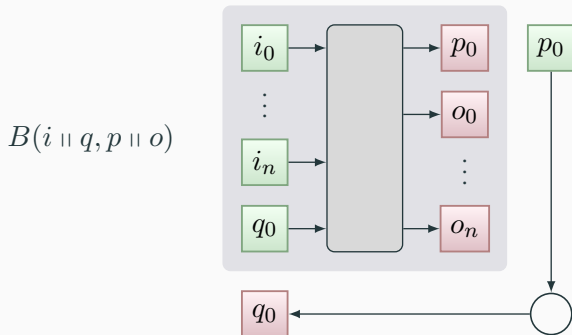


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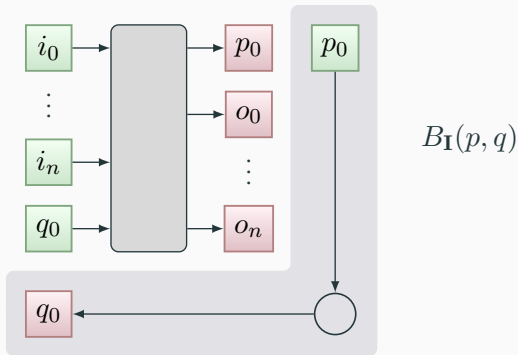


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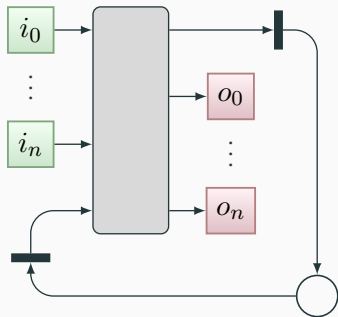


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Now what ?

- A network needs to be physically implemented somehow.
- Technology mapping tools need it in netlist form.
- How to ensure the netlist matches the block expression ?

An abstract representation

Idea !

Use an intermediate source with two possible targets.

- Let \mathbb{H} denote a universe of *hierarchical blocks*.
- Let \mathbb{L} denote a universe of netlists.
- Transform $\mathbb{B} \xleftarrow{\mathcal{T}_{\mathbb{H}\mathbb{B}}} \mathbb{H} \xrightarrow{\mathcal{T}_{\mathbb{H}\mathbb{L}}} \mathbb{L}$ in either direction.
- Make the transformations $\mathcal{T}_{\mathbb{H}\mathbb{B}}$ and $\mathcal{T}_{\mathbb{H}\mathbb{L}}$ simple and obvious.

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Structure-preserving maps between blockoids over \mathbb{H} , \mathbb{B} , and \mathbb{L} ?

A blockoid $(\mathbb{H}, \mathbf{R}, \mathbf{Z}, \mathbf{I})$ over hierarchical blocks

Define the universe of hierarchical blocks

$$\mathbb{H} = \mathbb{B} \cup \mathbb{H}^*$$

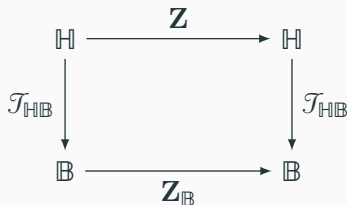
and let nested lists encode them by block combinators

$$\begin{aligned}\mathbf{I} &= \mathbf{I}_{\mathbb{B}} \\ \mathbf{Z}(x) &= \langle x \rangle \\ \mathbf{R}(\langle x \rangle, \langle y \rangle) &= \langle \langle x \rangle, \langle y \rangle \rangle \\ \mathbf{R}(\langle x \rangle, Y) &= \langle \langle x \rangle \parallel Y \rangle \\ \mathbf{R}(X, \langle y \rangle) &= X \parallel \langle \langle y \rangle \rangle \\ \mathbf{R}(X, Y) &= X \parallel Y\end{aligned}$$

for all $x, y \in \mathbb{H}$ and $X, Y \in \mathbb{H}^*$ with $|X|, |Y| > 1$.

Transformation from hierarchical to primitive blocks

A transformation $\mathcal{T}_{\mathbb{H}\mathbb{B}} : \mathbb{H} \rightarrow \mathbb{B}$ satisfying



determines a refinement relation

$$X \overset{\alpha}{\sqsubseteq} Y \Leftrightarrow \mathcal{T}_{\mathbb{H}\mathbb{B}} X \overset{\epsilon}{\sqsubseteq} \mathcal{T}_{\mathbb{H}\mathbb{B}} Y$$

and hence an extensional semantics for all $X, Y \in \mathbb{H}$.

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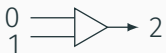
map over a list

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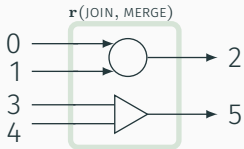
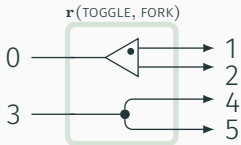
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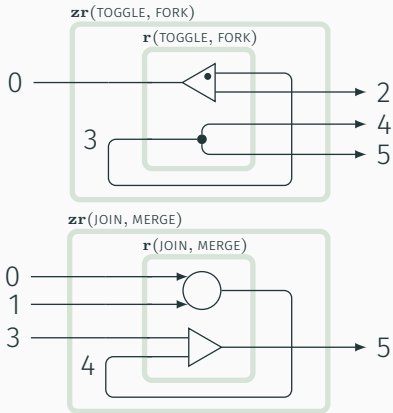
Netlists from blockoid operators



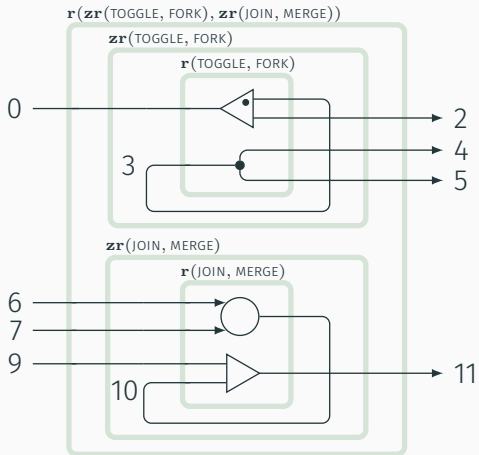
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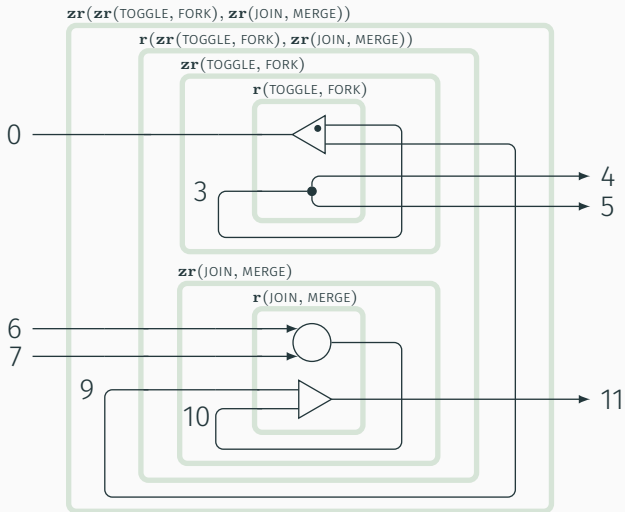
Netlists from blockoid operators



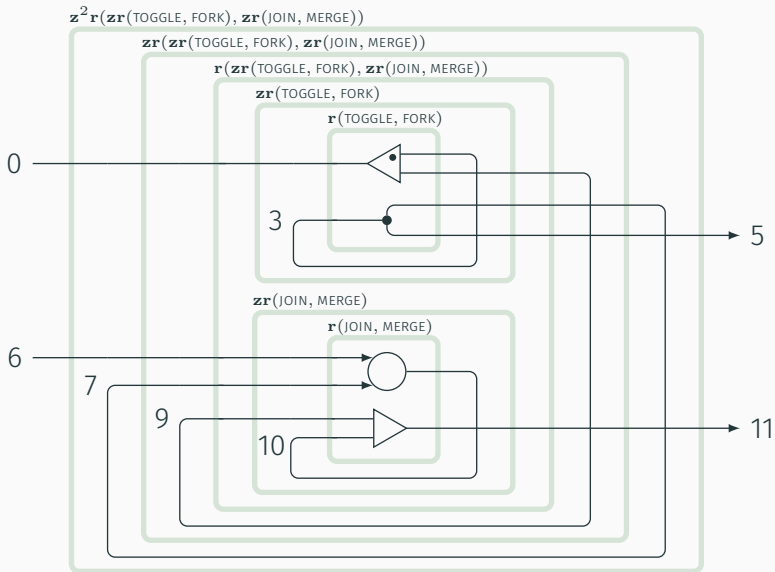
Netlists from blockoid operators



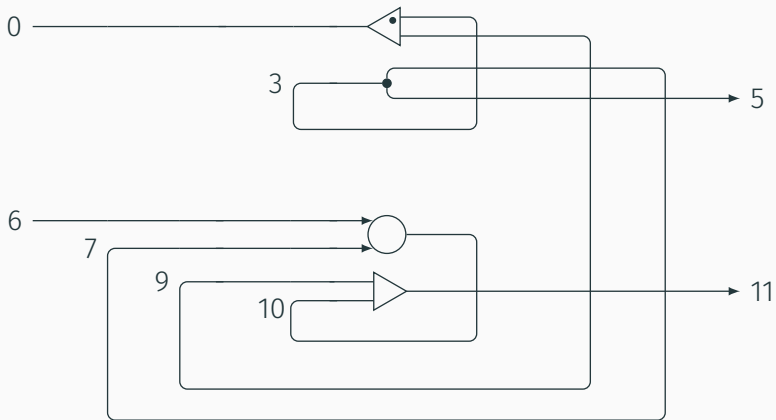
Netlists from blockoid operators



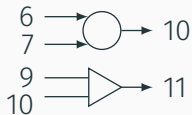
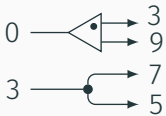
Netlists from blockoid operators



Netlists from blockoid operators



Netlists from blockoid operators



Netlists from blockoid operators



Netlists boil down to members of $(\mathbb{N}^* \times \mathbb{N}^* \times \mathbb{B})^*$ such as

<
 <0>, <3, 9>, TOGGLE),
 <3>, <7, 5>, FORK),
 <6, 7>, <10>, JOIN),
 <9, 10>, <11>, MERGE)>

A blockoid $(\mathbb{L}, \mathbf{R}_{\mathbb{L}}, \mathbf{Z}_{\mathbb{L}}, \mathbf{I}_{\mathbb{L}})$ over netlists

Define the universe of netlists

$$\mathbb{L} = (\mathbb{N}^* \times \mathbb{N}^* \times \mathbb{B})^*$$

with

$$\mathbf{I}_{\mathbb{L}} = \langle (\langle 0 \rangle, \langle 1 \rangle, \mathbf{I}_{\mathbb{B}}) \rangle$$

and $\mathbf{Z}_{\mathbb{L}}, \mathbf{R}_{\mathbb{L}}$ satisfying

$$\begin{array}{ccc} \mathbb{H} & \xrightarrow{\mathbf{Z}} & \mathbb{H} \\ \mathcal{J}_{\mathbb{H}\mathbb{L}} \downarrow & & \downarrow \mathcal{J}_{\mathbb{H}\mathbb{L}} \\ \mathbb{L} & \xrightarrow{\mathbf{Z}_{\mathbb{L}}} & \mathbb{L} \end{array}$$

A blockoid $(\mathbb{L}, \mathbf{R}_{\mathbb{L}}, \mathbf{Z}_{\mathbb{L}}, \mathbf{I}_{\mathbb{L}})$ over netlists

Define the universe of netlists

$$\mathbb{L} = (\mathbb{N}^* \times \mathbb{N}^* \times \mathbb{B})^*$$

with

$$\mathbf{I}_{\mathbb{L}} = \langle \langle \langle 0 \rangle, \langle 1 \rangle, \mathbf{I}_{\mathbb{B}} \rangle \rangle$$

and $\mathbf{Z}_{\mathbb{L}}, \mathbf{R}_{\mathbb{L}}$ satisfying

$$\begin{array}{ccc} \mathbb{H} \times \mathbb{H} & \xrightarrow{\mathbf{R}} & \mathbb{H} \\ \lambda(x, y). (\mathcal{T}_{\mathbb{H}\mathbb{L}} x, \mathcal{T}_{\mathbb{H}\mathbb{L}} y) \downarrow & & \downarrow \mathcal{T}_{\mathbb{H}\mathbb{L}} \\ \mathbb{L} \times \mathbb{L} & \xrightarrow{\mathbf{R}_{\mathbb{L}}} & \mathbb{L} \end{array}$$

A blockoid $(\mathbb{L}, \mathbf{R}_{\mathbb{L}}, \mathbf{Z}_{\mathbb{L}}, \mathbf{I}_{\mathbb{L}})$ over netlists

Define the universe of netlists

$$\mathbb{L} = (\mathbb{N}^* \times \mathbb{N}^* \times \mathbb{B})^*$$

with

$$\mathbf{I}_{\mathbb{L}} = \langle \langle \langle 0 \rangle, \langle 1 \rangle, \mathbf{I}_{\mathbb{B}} \rangle \rangle$$

and $\mathbf{Z}_{\mathbb{L}}, \mathbf{R}_{\mathbb{L}}$ satisfying

$$\mathcal{J}_{\mathbb{H}\mathbb{L}}(h) = \begin{cases} (\lambda(I, O, B). \langle \langle \iota_I, \iota_O^I, h \rangle \rangle) h & \text{if } h \in \mathbb{B} \\ \mathbf{Z}_{\mathbb{L}} \mathcal{J}_{\mathbb{H}\mathbb{L}} h_0 & \text{if } h \in \mathbb{H}^1 \\ (F \mathbf{R}_{\mathbb{L}}) \mathcal{J}_{\mathbb{H}\mathbb{L}}^* h & \text{otherwise} \end{cases}$$

A blockoid $(\mathbb{L}, \mathbf{R}_{\mathbb{L}}, \mathbf{Z}_{\mathbb{L}}, \mathbf{I}_{\mathbb{L}})$ over netlists

Define the universe of netlists

$$\mathbb{L} = (\mathbb{N}^* \times \mathbb{N}^* \times \mathbb{B})^*$$

with

$$\mathbf{I}_{\mathbb{L}} = \langle \langle \langle 0 \rangle, \langle 1 \rangle, \mathbf{I}_{\mathbb{B}} \rangle \rangle$$

and $\mathbf{Z}_{\mathbb{L}}, \mathbf{R}_{\mathbb{L}}$ satisfying

$$\mathcal{J}_{\mathbb{H}\mathbb{L}}(h) = \begin{cases} (\lambda(I, O, B). \langle (\iota_I, \iota_O^I, h) \rangle) h & \text{if } h \in \mathbb{B} \\ \mathbf{Z}_{\mathbb{L}} \mathcal{J}_{\mathbb{H}\mathbb{L}} h_0 & \text{if } h \in \mathbb{H}^1 \\ (F \mathbf{R}_{\mathbb{L}}) \mathcal{J}_{\mathbb{H}\mathbb{L}}^* h & \text{otherwise} \end{cases}$$

A blockoid $(\mathbb{L}, \mathbf{R}_{\mathbb{L}}, \mathbf{Z}_{\mathbb{L}}, \mathbf{I}_{\mathbb{L}})$ over netlists

For input terminal numbers and output terminal numbers

$$i(x) = \bigcup_{(I,O,B) \in \mathcal{R}(x)} \mathcal{R}(I) \qquad o(x) = \bigcup_{(I,O,B) \in \mathcal{R}(x)} \mathcal{R}(O)$$

associated with a netlist $x \in \mathbb{L}$, we have

<i>all terminals</i>	$i(x) \cup o(x)$
<i>all external inputs</i>	$i(x) - o(x)$
<i>all external outputs</i>	$o(x) - i(x)$
<i>first external output</i>	$\min(o(x) - i(x))$
<i>last external input</i>	$\max(i(x) - o(x))$

A blockoid $(\mathbb{L}, \mathbf{R}_{\mathbb{L}}, \mathbf{Z}_{\mathbb{L}}, \mathbf{I}_{\mathbb{L}})$ over netlists

A rewrite rule for input terminal numbers

$$w_z(x) = \lambda t. \begin{cases} \min(o(x) - i(x)) & \text{if } t = \max(i(x) - o(x)) \\ t & \text{otherwise} \end{cases}$$

specifies the operator $\mathbf{Z}_{\mathbb{L}} : \mathbb{L} \rightarrow \mathbb{L}$

$$\mathbf{Z}_{\mathbb{L}}(x) = (\lambda(I, O, B). ((w_z x)^* I, O, B))^* x$$

A blockoid $(\mathbb{L}, \mathbf{R}_{\mathbb{L}}, \mathbf{Z}_{\mathbb{L}}, \mathbf{I}_{\mathbb{L}})$ over netlists

A rewrite rule taking $t \in \mathbb{N}$ to a number outside $i(x) \cup o(x)$

$$w_r(x) = \lambda t. t + 1 + \max(i(x) \cup o(x))$$

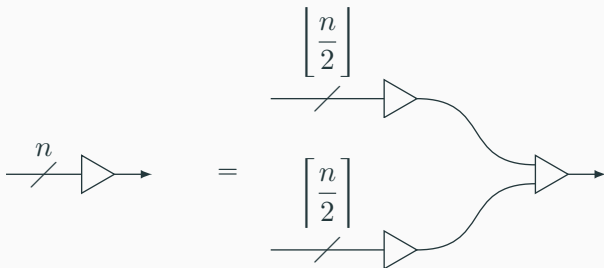
specifies the operator $\mathbf{R}_{\mathbb{L}} : \mathbb{L} \times \mathbb{L} \rightarrow \mathbb{L}$

$$\mathbf{R}_{\mathbb{L}}(x, y) = x \parallel (\lambda(I, O, B). ((w_r x)^* I, (w_r x)^* O, B))^* y$$

Taking it for a spin

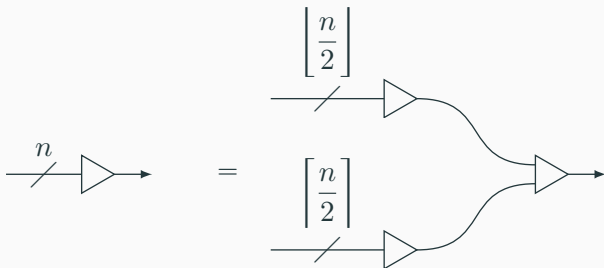
Multiway merge

$$f(n) = \begin{cases} \mathbf{I} & \text{if } n = 1 \\ \mathbf{Z}^2 \mathbf{R}(\mathbf{R}(f[\lfloor n/2 \rfloor], f[\lfloor n/2 \rfloor]), \text{MERGE}) & \text{otherwise} \end{cases}$$



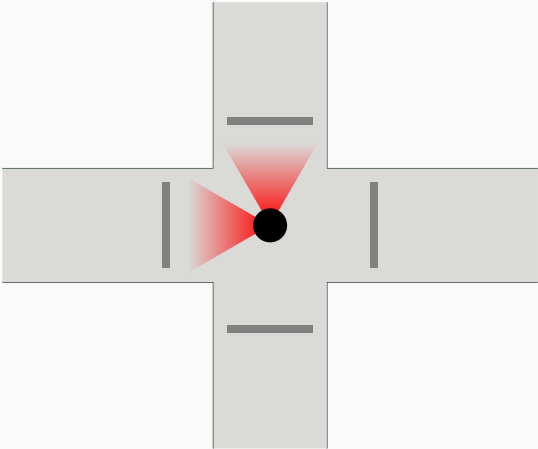
Multiway merge

$$f(n) = \begin{cases} \mathbf{I} & \text{if } n = 1 \\ \mathbf{Z}^2 \mathbf{R}(\mathbf{R}(f[\lfloor n/2 \rfloor], f[\lfloor n/2 \rfloor]), \text{MERGE}) & \text{otherwise} \end{cases}$$

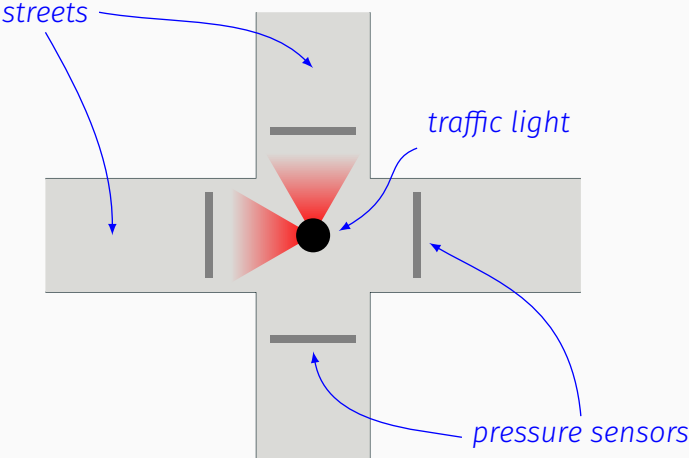


Too easy ?

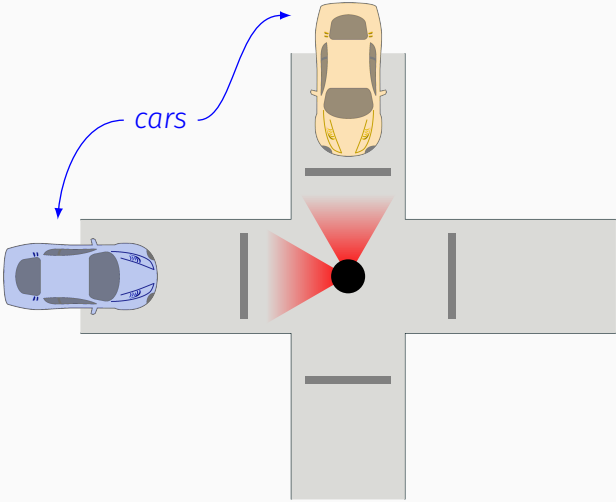
Traffic control



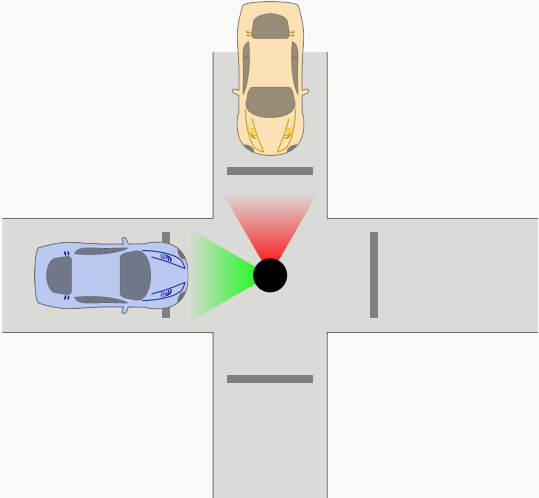
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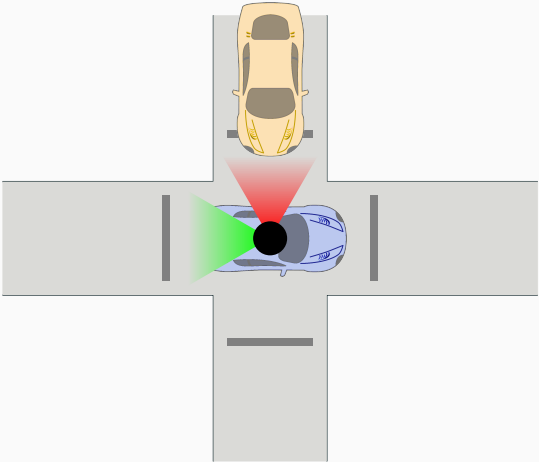
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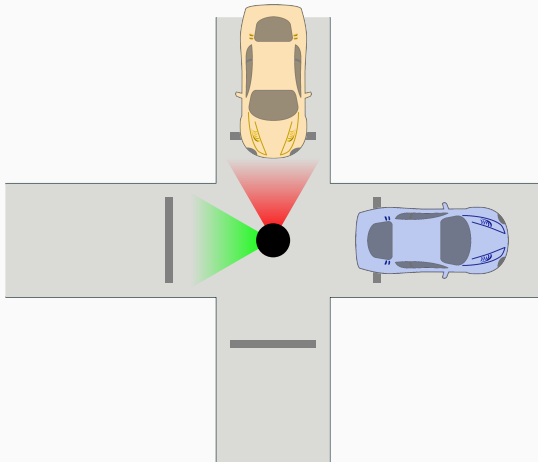
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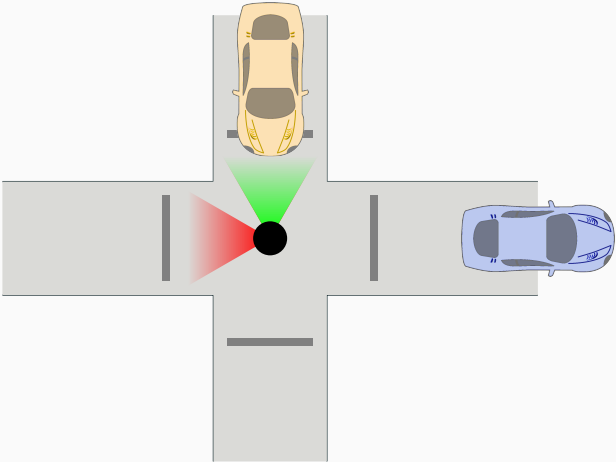
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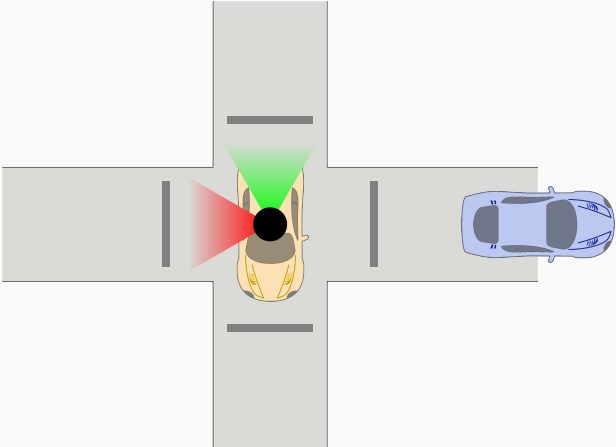
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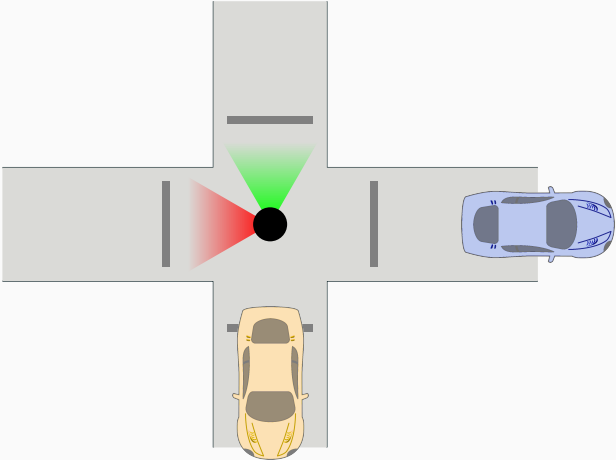
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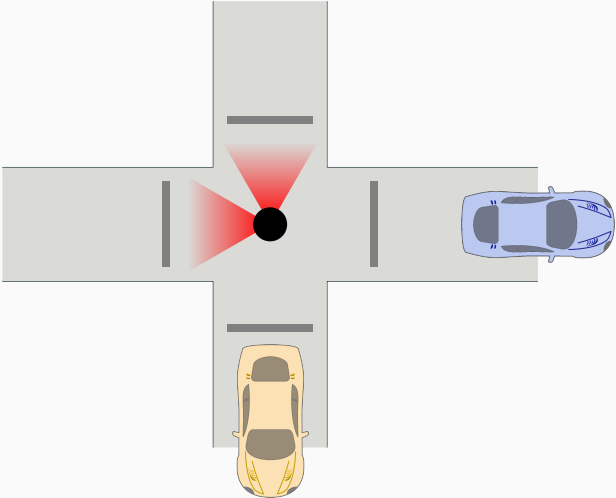
Traffic control



Traffic control



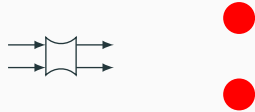
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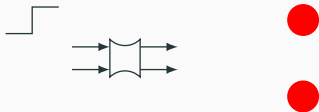
Arbiter circuits



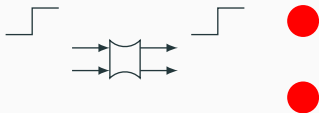
Arbiter circuits



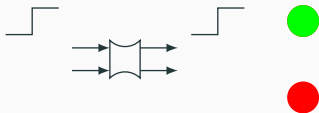
Arbiter circuits



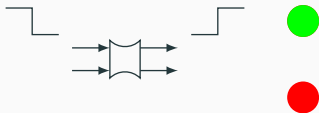
Arbiter circuits



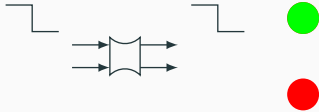
Arbiter circuits



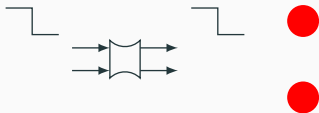
Arbiter circuits



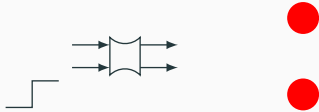
Arbiter circuits



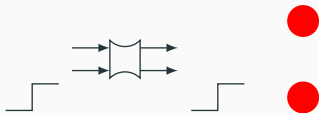
Arbiter circuits



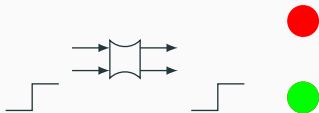
Arbiter circuits



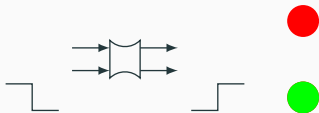
Arbiter circuits



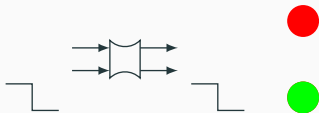
Arbiter circuits



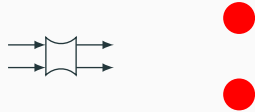
Arbiter circuits



Arbiter circuits



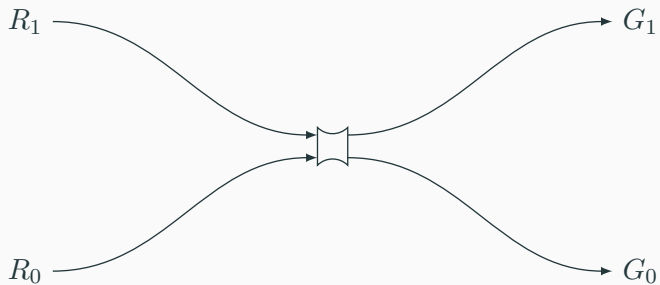
Arbiter circuits



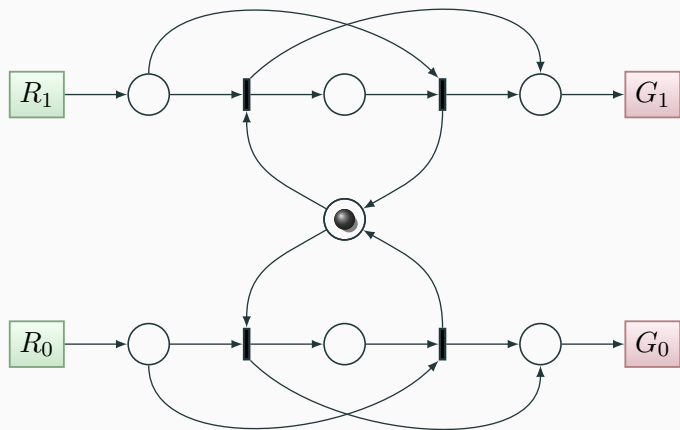
Arbiter circuits



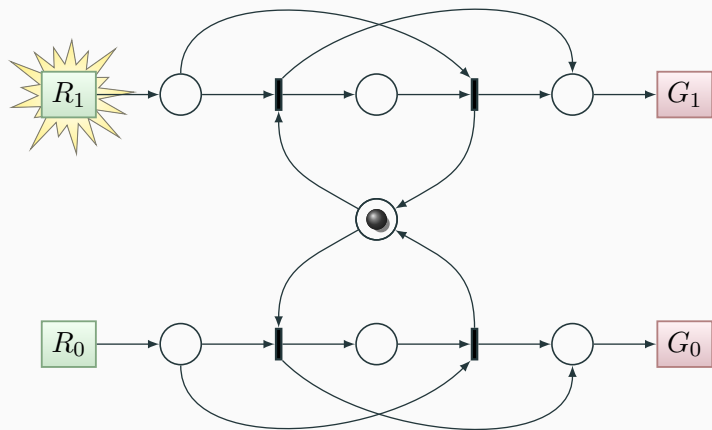
Arbiter circuits



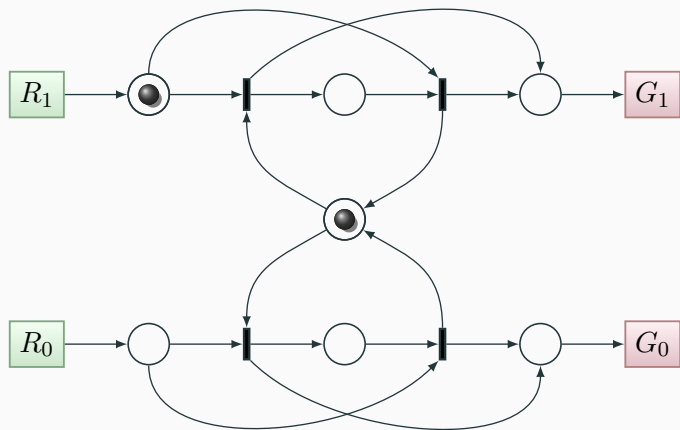
Arbiter circuits



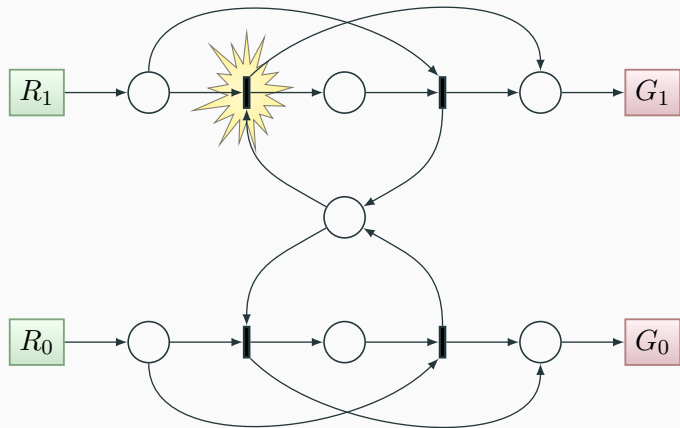
Arbiter circuits



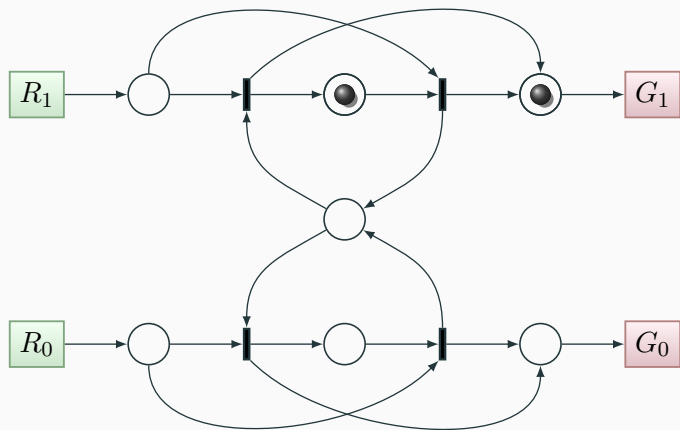
Arbiter circuits



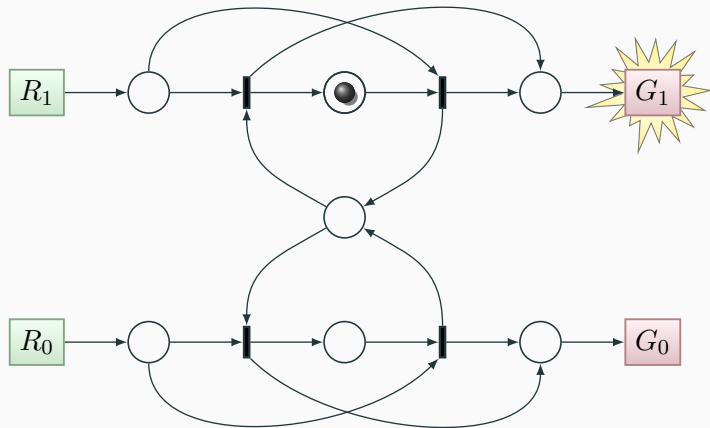
Arbiter circuits



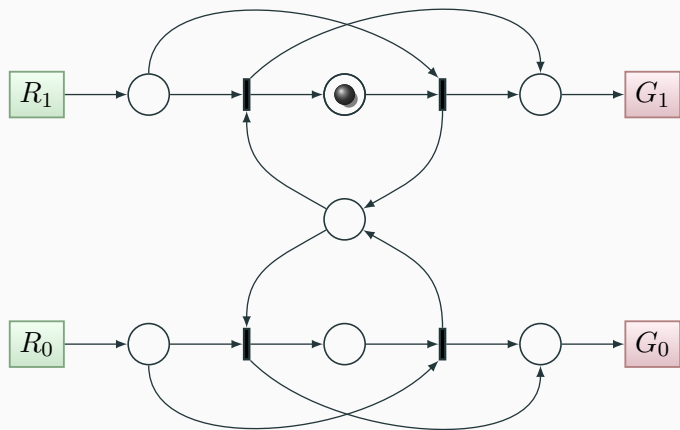
Arbiter circuits



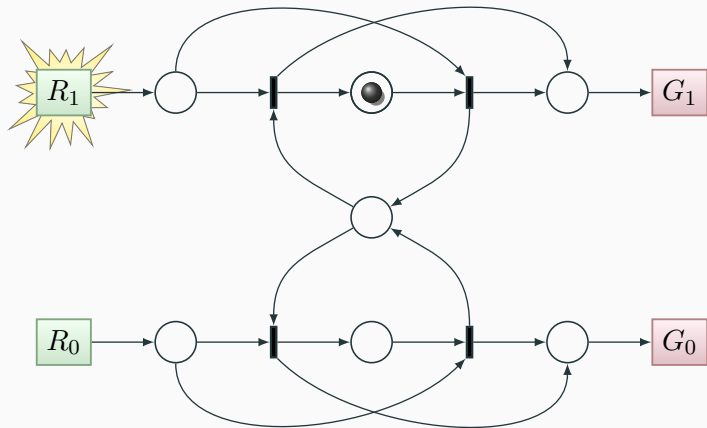
Arbiter circuits



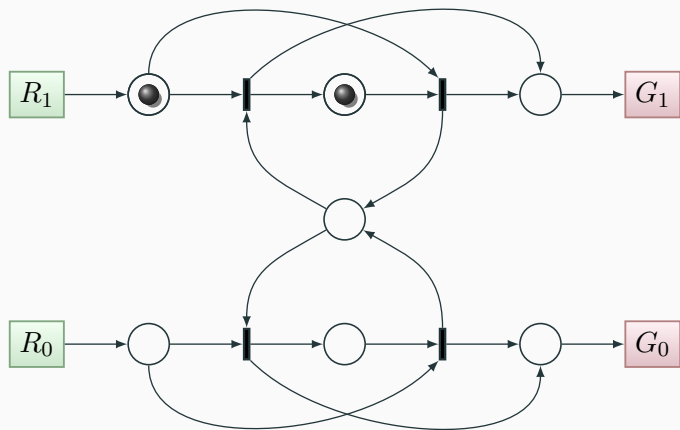
Arbiter circuits



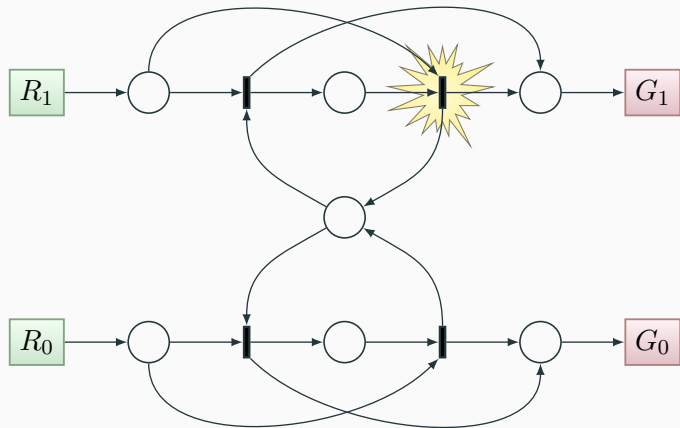
Arbiter circuits



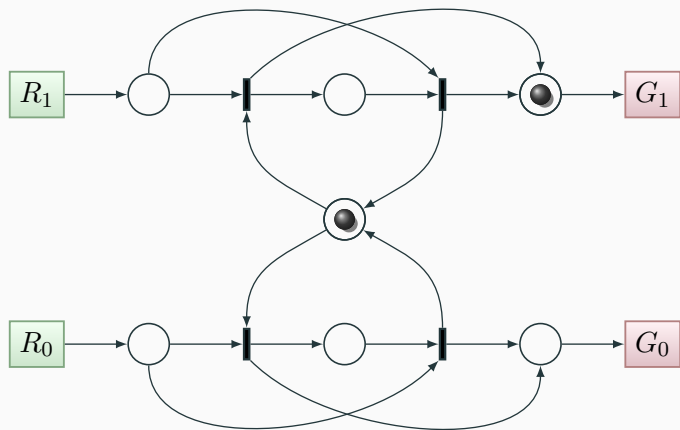
Arbiter circuits



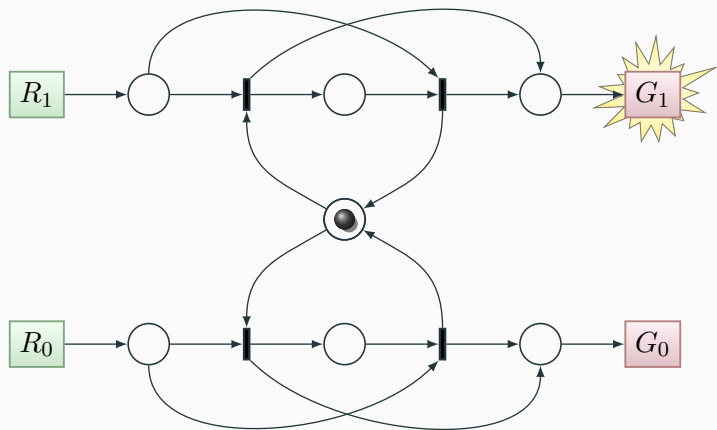
Arbiter circuits



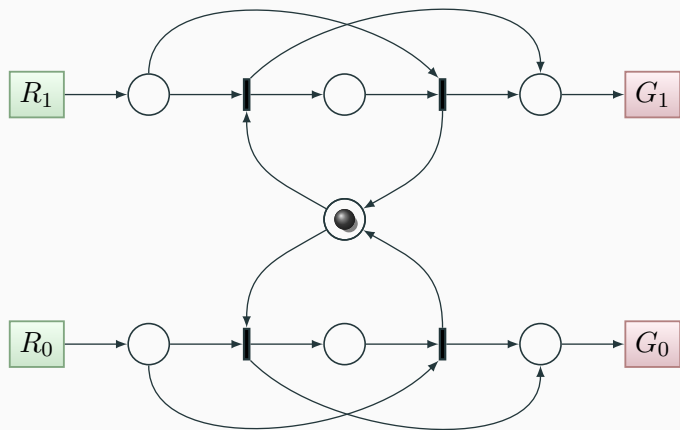
Arbiter circuits



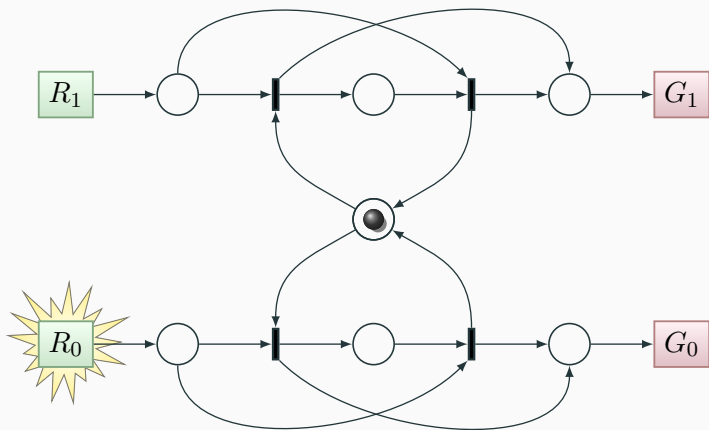
Arbiter circuits



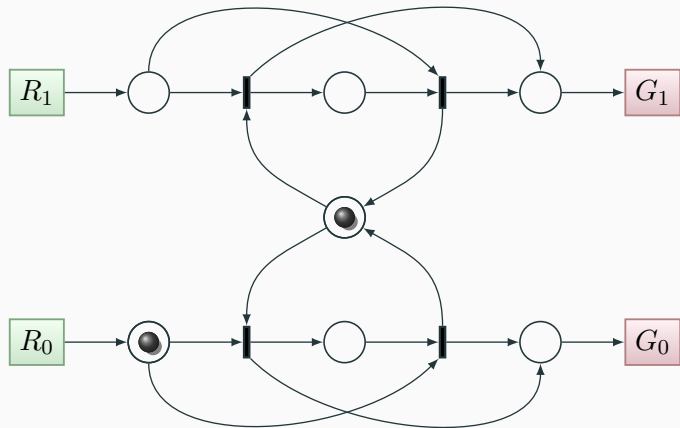
Arbiter circuits



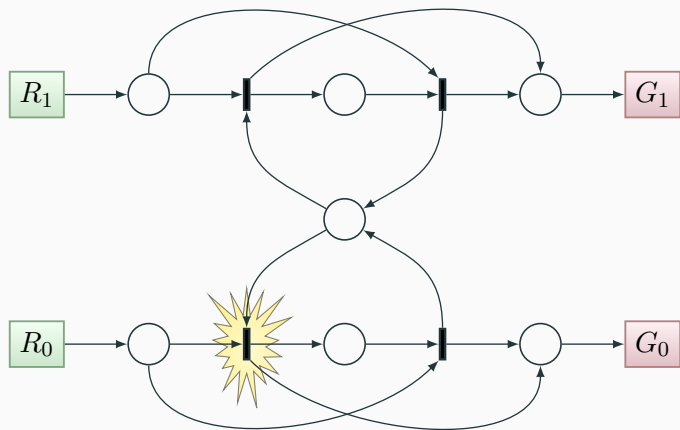
Arbiter circuits



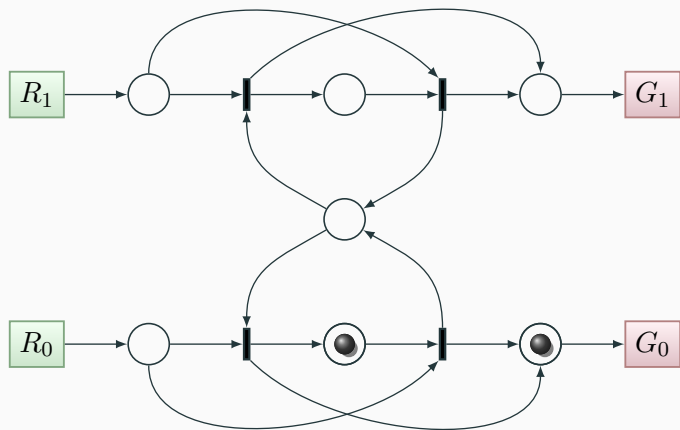
Arbiter circuits



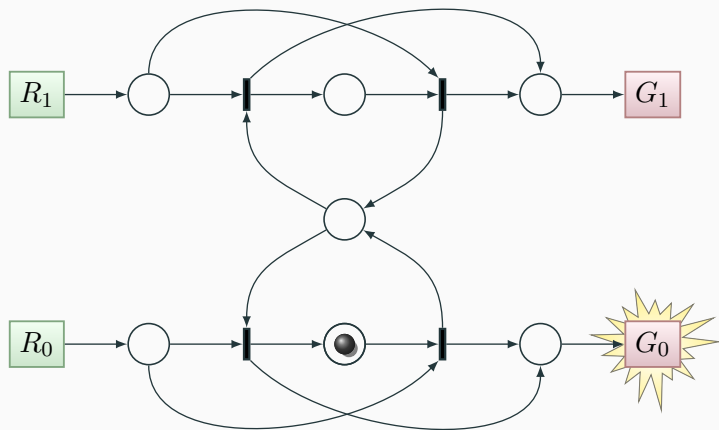
Arbiter circuits



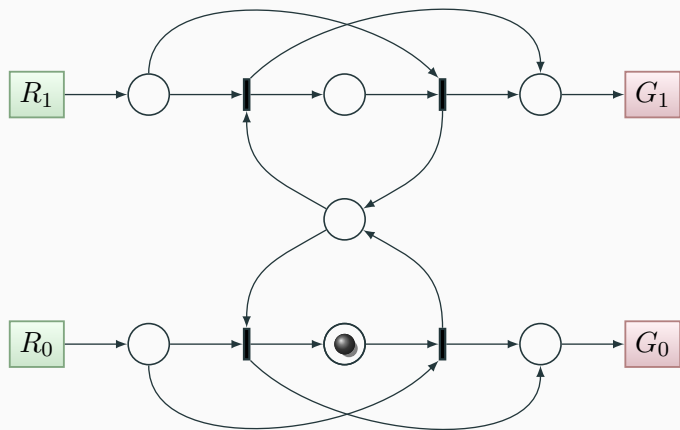
Arbiter circuits



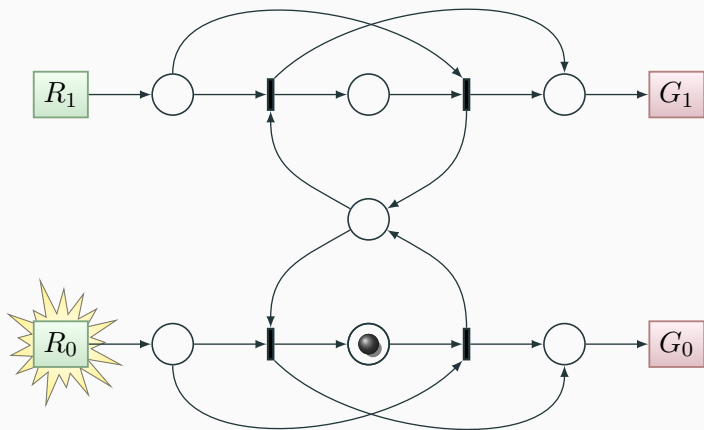
Arbiter circuits



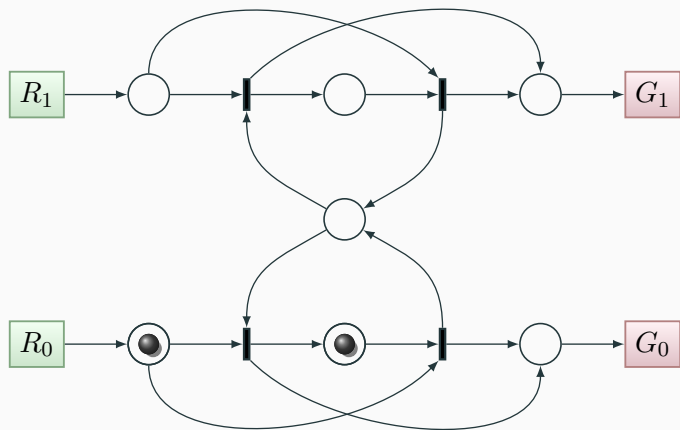
Arbiter circuits



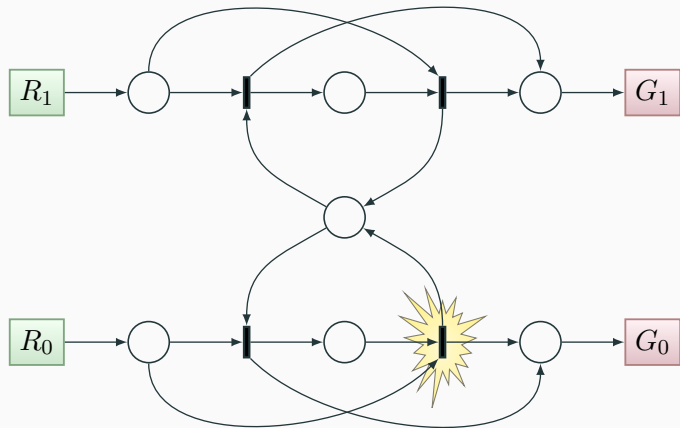
Arbiter circuits



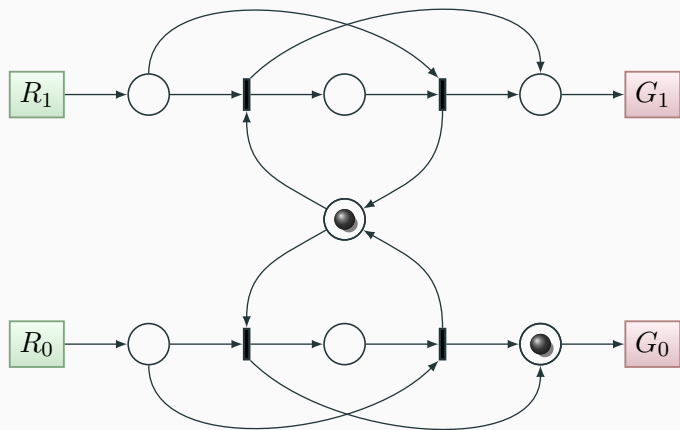
Arbiter circuits



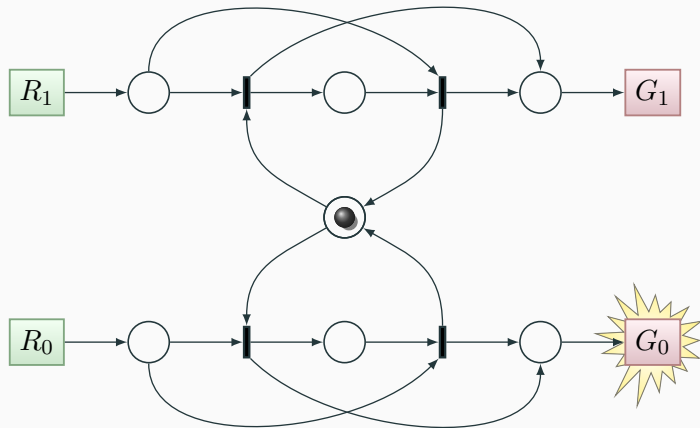
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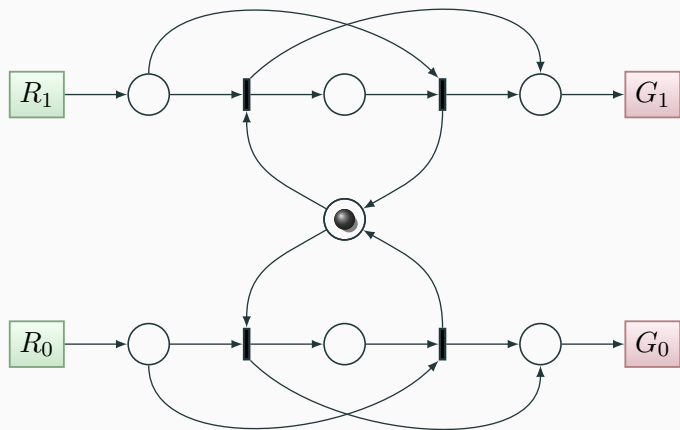
Arbiter circuits



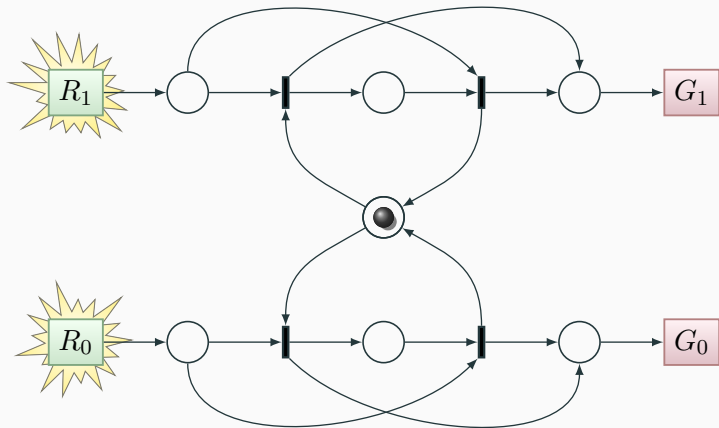
Arbiter circuits



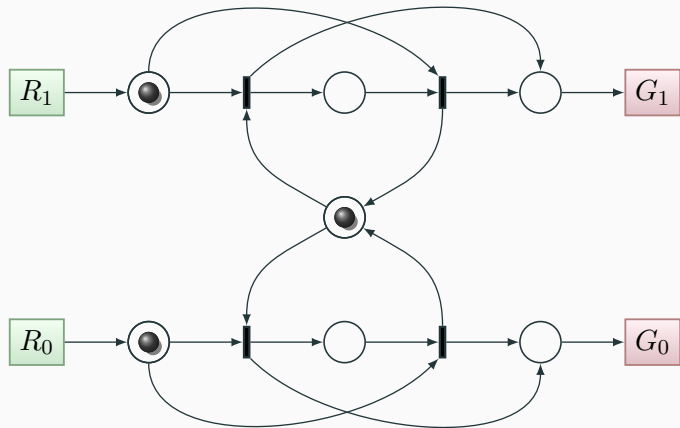
Arbiter circuits



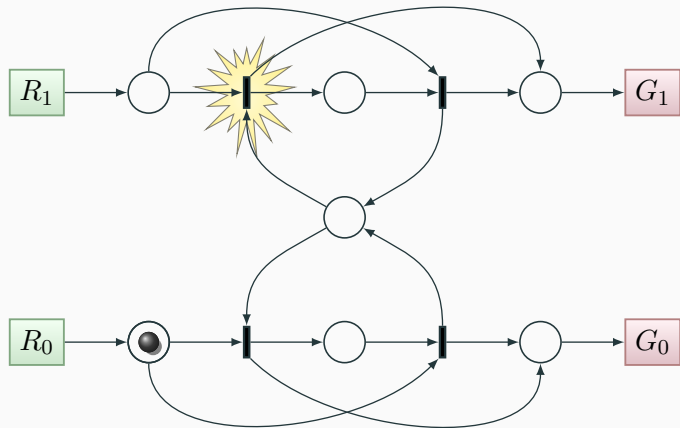
Arbiter circuits



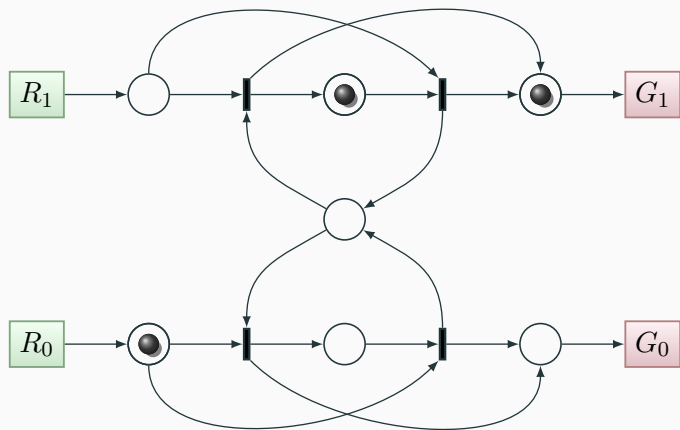
Arbiter circuits



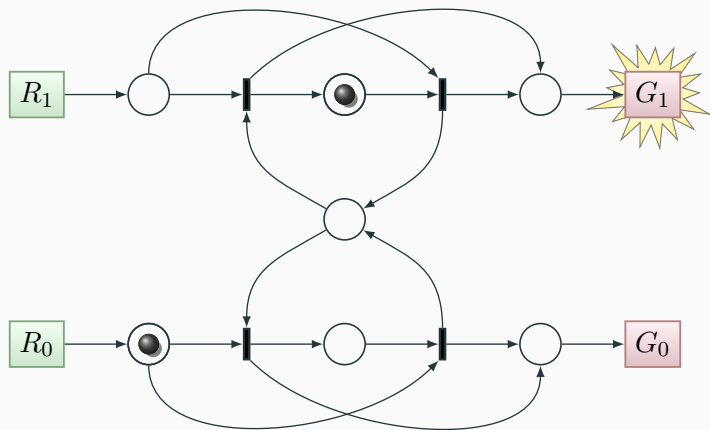
Arbiter circuits



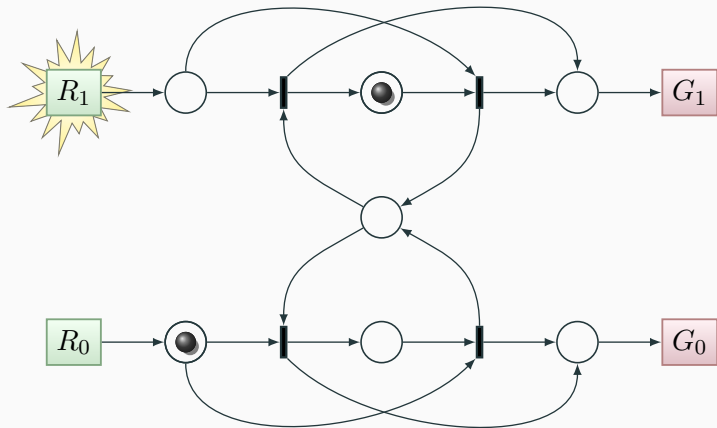
Arbiter circuits



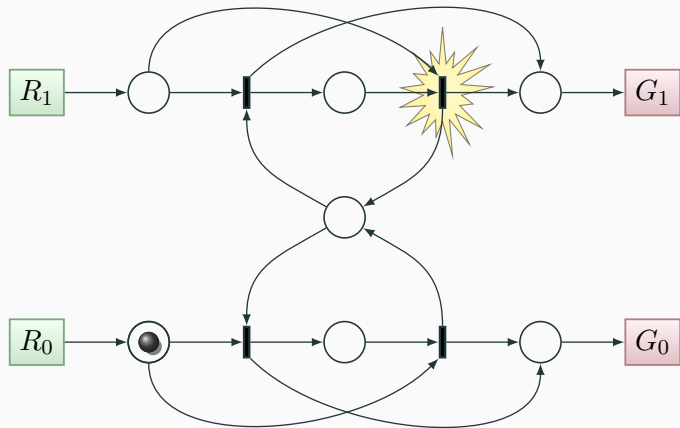
Arbiter circuits



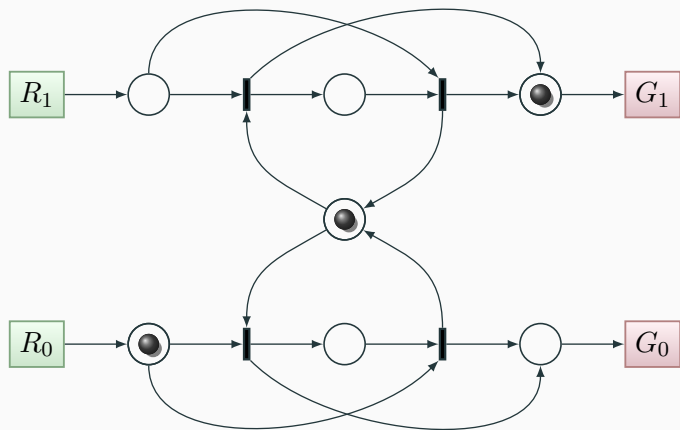
Arbiter circuits



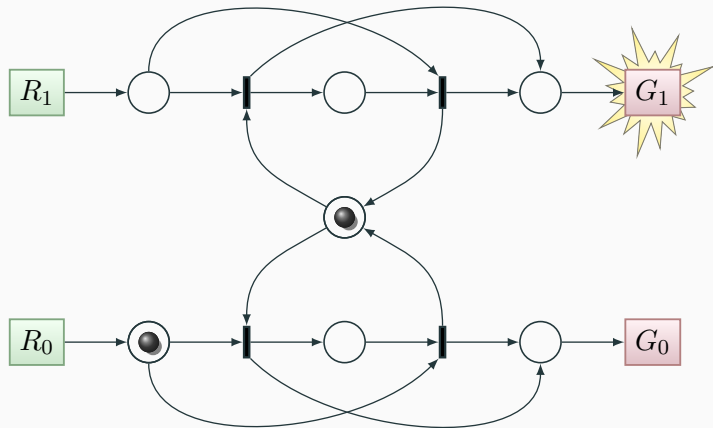
Arbiter circuits



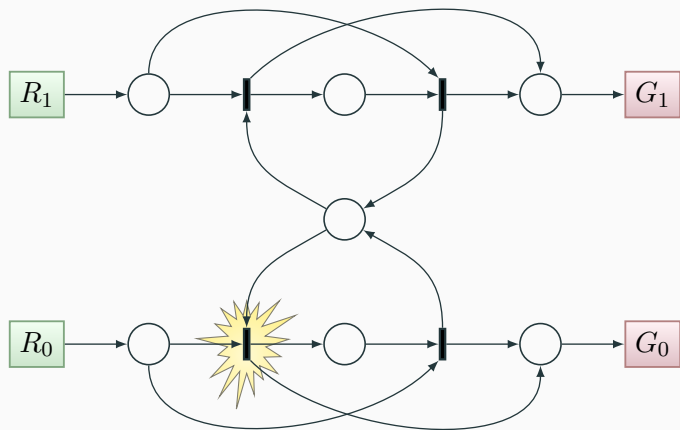
Arbiter circuits



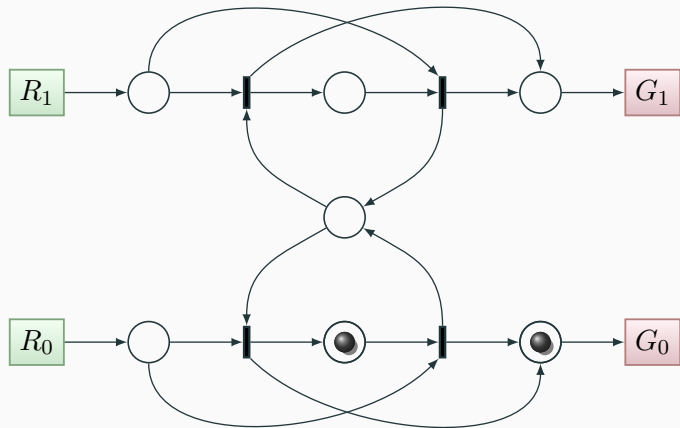
Arbiter circuits



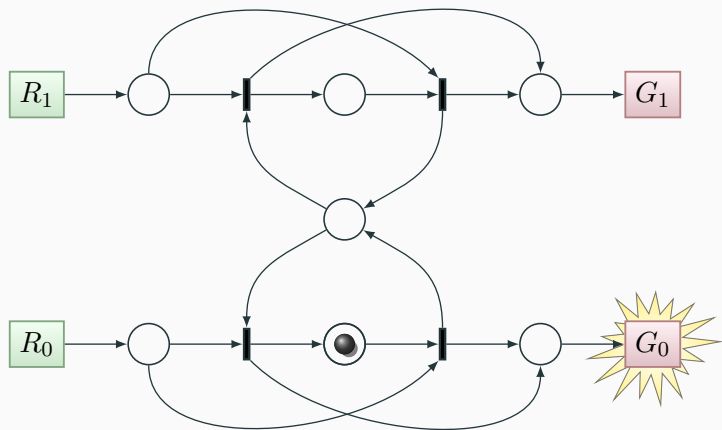
Arbiter circuits



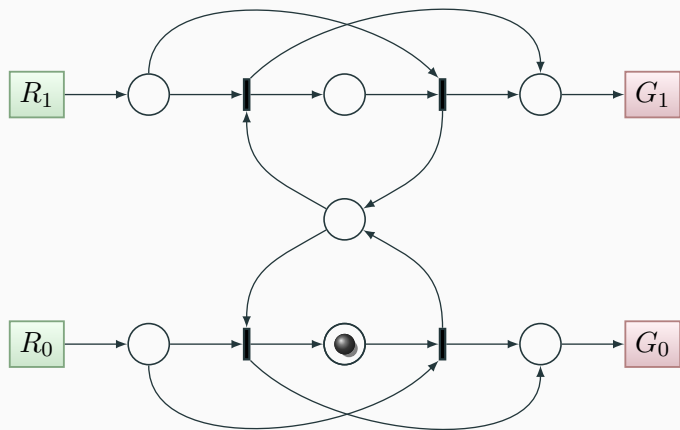
Arbiter circuits



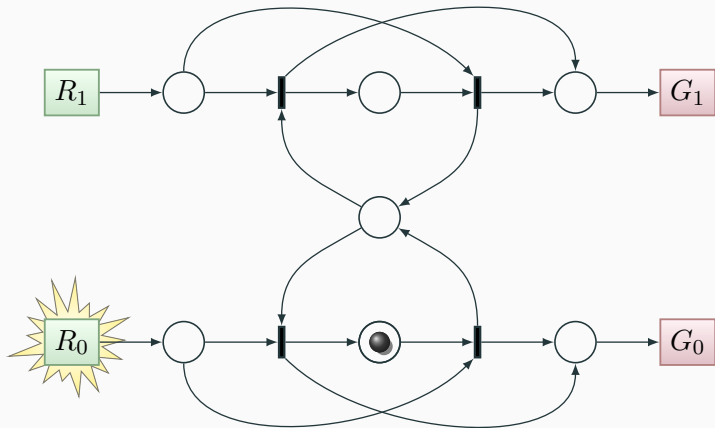
Arbiter circuits



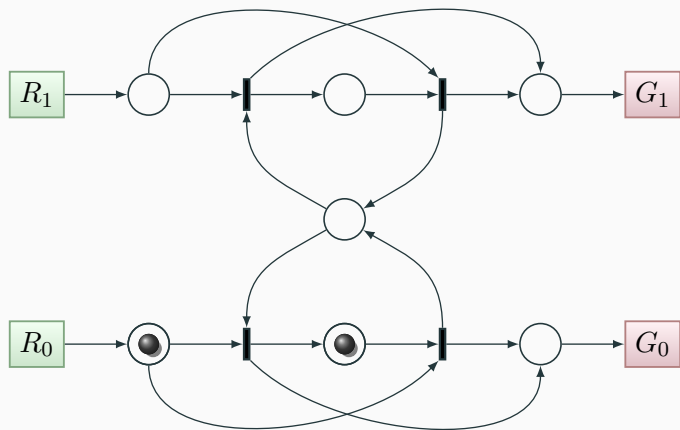
Arbiter circuits



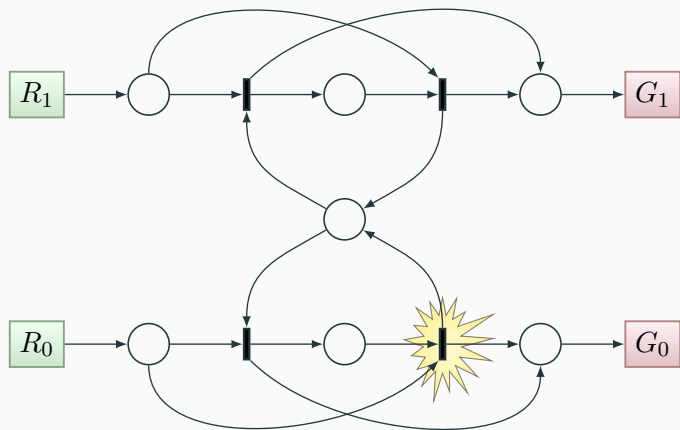
Arbiter circuits



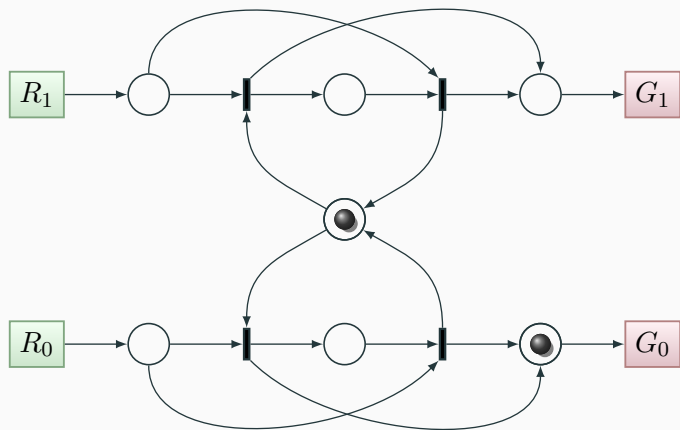
Arbiter circuits



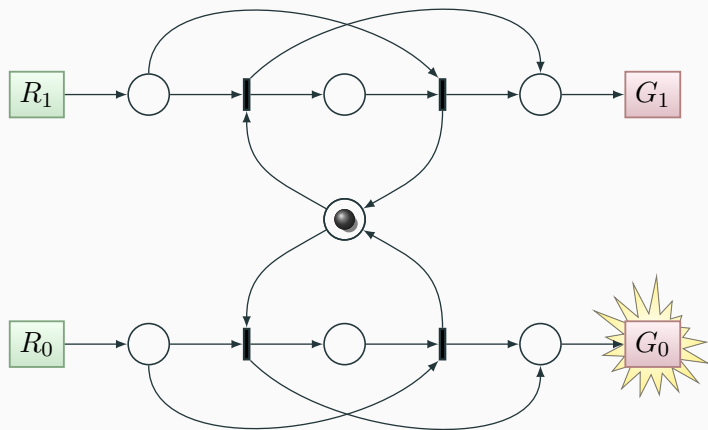
Arbiter circuits



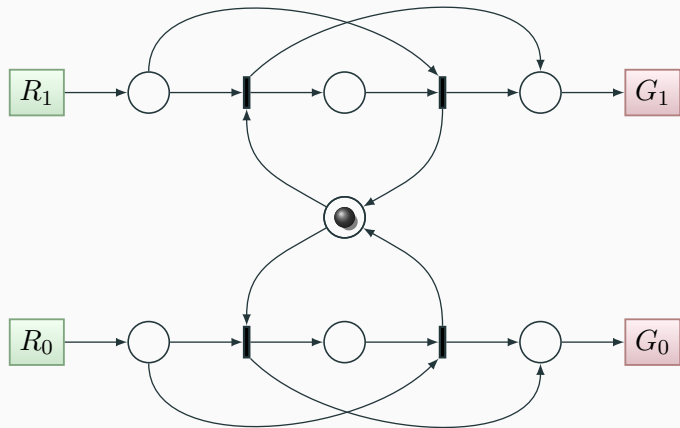
Arbiter circuits



Arbiter circuits



Arbiter circuits



A job for a 4-way arbiter



Each player's button pre-empt's the other players.

A job for a 4-way arbiter



Each player's button pre-empt's the other players.

A job for a 4-way arbiter



Each player's button pre-empt's the other players.

A job for a 4-way arbiter



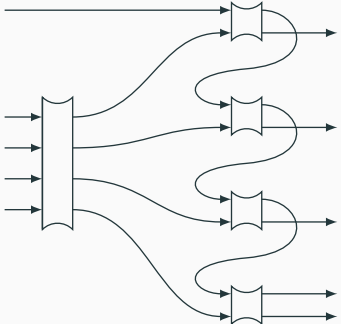
Each player's button pre-empt's the other players.

A job for a 4-way arbiter



Each player's button pre-empt's the other players.

Triangle mesh arbiter



Triangle mesh arbiter

$f(n) \in \mathbb{H}$ expresses an n -way triangle mesh arbiter by

$$f(1) = \mathbf{I}$$

$$f(n+1) = \mathbf{SZ}^n \mathbf{R}(f n, (F_{\mathbf{I}} \lambda(h, t). \mathbf{ZR}(\mathbf{ZR}(\text{ARB}, \mathbf{S}^{n-h} t), \mathbf{I})) \iota_n^1)$$

where $\mathbf{S} : \mathbb{H} \rightarrow \mathbb{H}$ rolls down the inputs

$$\mathbf{S} = \lambda x. \mathbf{ZR}(\mathbf{I}, x)$$

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where $\mathbf{S} : \mathbb{H} \rightarrow \mathbb{H}$ rolls down the inputs

$$\mathbf{S} = \lambda x. \mathbf{ZR}(\mathbf{I}, x)$$

For example, letting $n = 4$ and

$$g = \lambda(h, t). \mathbf{ZR}(\mathbf{ZR}(\text{ARB}, \mathbf{S}^{4-h} t), \mathbf{I})$$

we have

$$f(5) = \mathbf{SZ}^4 \mathbf{R}(f\ 4, (F_{\mathbf{I}} \lambda(h, t). \mathbf{ZR}(\mathbf{ZR}(\text{ARB}, \mathbf{S}^{4-h} t), \mathbf{I})) \iota_4^1)$$

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we have

$$f(5) = \mathbf{SZ}^4 \mathbf{R}(f\ 4, g(1, g(2, g(3, g(4, \mathbf{I}))))))$$

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we have

$$f(5) = \mathbf{SZZZR}(f\ 4, g(1, g(2, g(3, g(4, \mathbf{I}))))$$

evaluating $f(5)$ from the inside out ...

$$\begin{aligned}g(4, \mathbf{I}) &= (\lambda(h, t) \cdot \mathbf{ZR}(\mathbf{ZR}(\text{ARB}, \mathbf{S}^{4-h} t), \mathbf{I})) (4, \mathbf{I}) \\ &= \mathbf{ZR}(\mathbf{ZR}(\text{ARB}, \mathbf{S}^0 \mathbf{I}), \mathbf{I}) \\ &\stackrel{?}{\equiv} \text{ARB}\end{aligned}$$

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$$\mathbf{I} = t = \longrightarrow$$

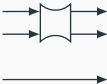
Triangle mesh arbiter

$t = \longrightarrow$

Triangle mesh arbiter

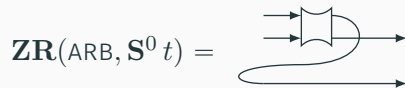
$$S^0 t = \longrightarrow$$

Triangle mesh arbiter

$$\mathbf{R}(\text{ARB}, \mathbf{S}^0 t) =$$


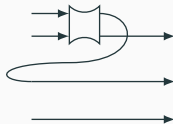
The diagram shows a component with two input arrows on the left and two output arrows on the right. The component is represented by a central shape with a concave left side and a convex right side, resembling a lens or a stylized letter 'B'. Below this component is a single horizontal arrow pointing to the right.

Triangle mesh arbiter

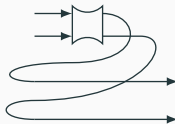


Triangle mesh arbiter

$\mathbf{R}(\mathbf{ZR}(\text{ARB}, \mathbf{S}^0 t), \mathbf{I}) =$



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$$g(4, \mathbf{I}) = t = \begin{array}{c} \rightarrow \\ \rightarrow \end{array} \begin{array}{c} \text{---} \\ \text{---} \end{array} \begin{array}{c} \rightarrow \\ \rightarrow \end{array}$$

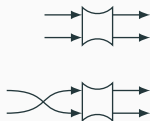
Triangle mesh arbiter

$$St = \text{[Diagram of a triangle mesh arbiter symbol]}$$

The diagram shows a symbol for a triangle mesh arbiter. It consists of two input lines on the left that cross each other, and two output lines on the right that are parallel. The symbol is enclosed in a rectangular box with a double-line border.

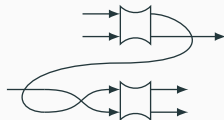
Triangle mesh arbiter

$\mathbf{R}(\text{ARB}, \mathbf{S}t) =$



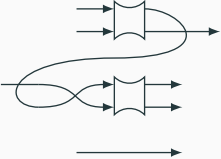
Triangle mesh arbiter

$\mathbf{ZR}(\text{ARB}, \mathbf{S} t) =$

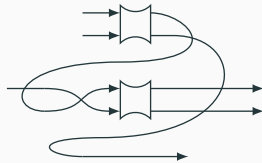


Triangle mesh arbiter

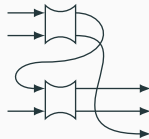
$$\mathbf{R}(\mathbf{Z}\mathbf{R}(\text{ARB}, \mathbf{S}t), \mathbf{I}) =$$

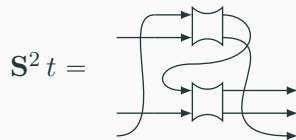


$\mathbf{ZR}(\mathbf{ZR}(\text{ARB}, \mathbf{S}t), \mathbf{I}) =$



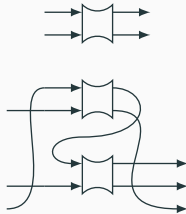
$$g(3, g(4, \mathbf{I})) = t =$$





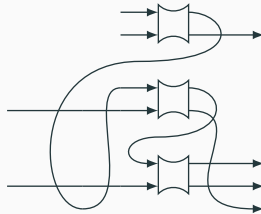
Triangle mesh arbiter

$\mathbf{R}(\text{ARB}, \mathbf{S}^2 t) =$



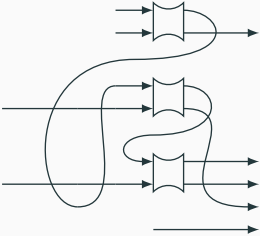
Triangle mesh arbiter

$\mathbf{ZR}_{(\text{ARB}, \mathbf{S}^2 t)} =$



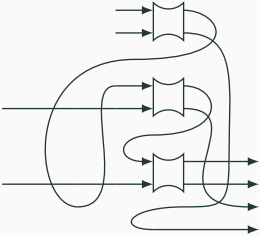
Triangle mesh arbiter

$$\mathbf{R}(\mathbf{ZR}(\text{ARB}, \mathbf{S}^2 t), \mathbf{I}) =$$



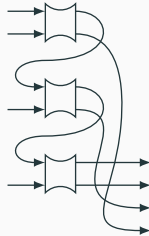
Triangle mesh arbiter

$$\mathbf{ZR}(\mathbf{ZR}(\text{ARB}, \mathbf{S}^2 t), \mathbf{I}) =$$

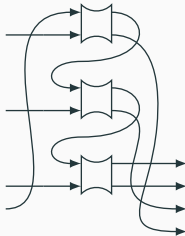


Triangle mesh arbiter

$$g(2, g(3, g(4, \mathbf{I}))) = t =$$

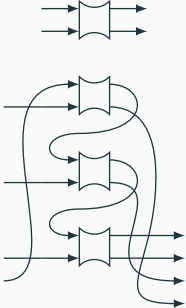


$S^3 t =$



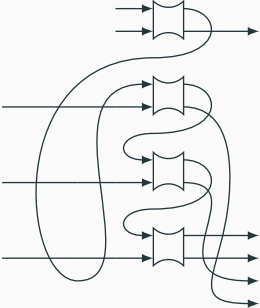
Triangle mesh arbiter

$$\mathbf{R}_{(\text{ARB}, \mathbf{S}^3 t)} =$$



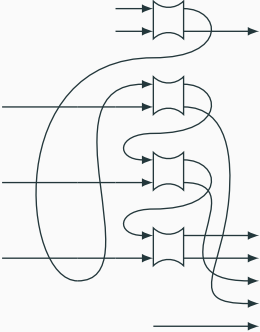
Triangle mesh arbiter

$$\mathbf{ZR}_{(\text{ARB}, \mathbf{S}^3 t)} =$$



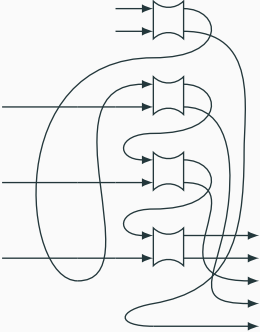
Triangle mesh arbiter

$$\mathbf{R}(\mathbf{ZR}(\text{ARB}, \mathbf{S}^3 t), \mathbf{I}) =$$

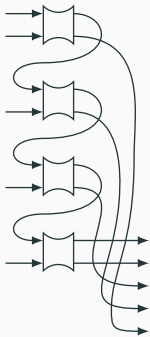


Triangle mesh arbiter

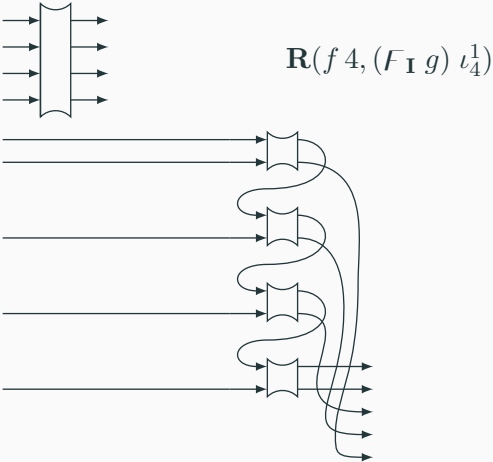
$$\mathbf{ZR}(\mathbf{ZR}(\text{ARB}, \mathbf{S}^3 t), \mathbf{I}) =$$



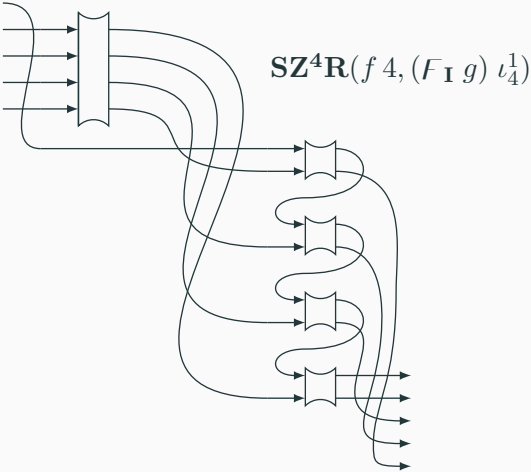
$$g(1, g(2, g(3, g(4, \mathbf{I})))) =$$



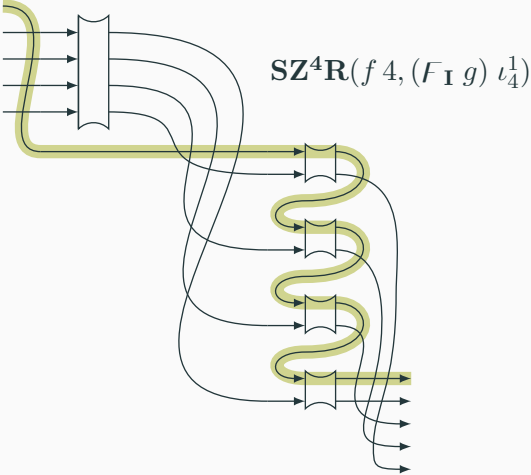
Triangle mesh arbiter



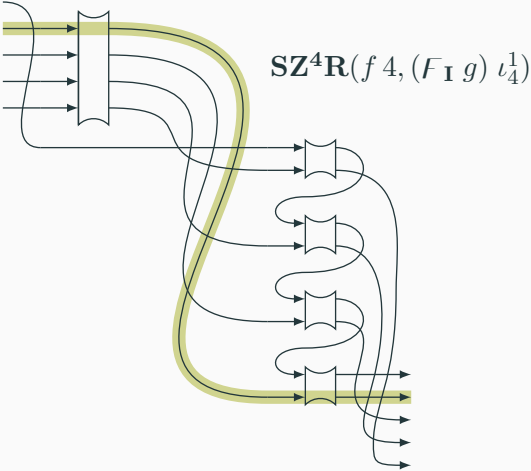
Triangle mesh arbiter



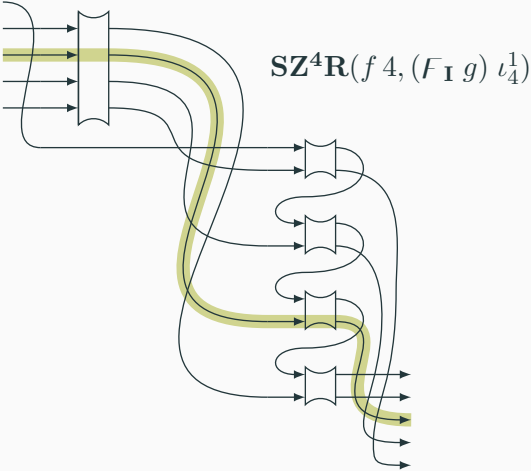
Triangle mesh arbiter



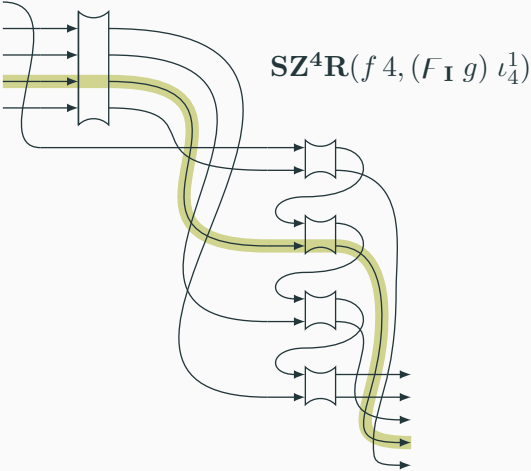
Triangle mesh arbiter



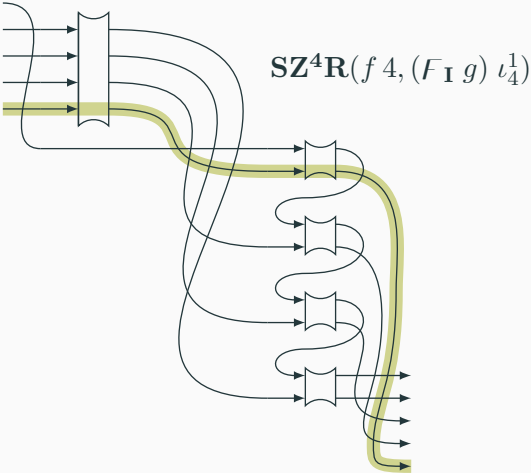
Triangle mesh arbiter



Triangle mesh arbiter



Triangle mesh arbiter



Summary

- express families of complicated circuits generally
- automatically generate checkable DI semantic models
- automatically generate corresponding netlists

Further reading

- <https://www.delayinsensitive.com>
 - full details on everything in this presentation
- <https://statebox.org>
 - overlapping ideas, more ambitious goals
- *Oliver Heaviside : The Life, Work and Times of an Electrical Genius of the Victorian Age*, Paul J. Nahin
 - historical perspective on formal methods

